

SEMICONDUCTOR TECHNICAL DATA

KIC7W125FK

SILICON MONOLITHIC CMOS DIGITAL INTEGRATED CIRCUIT

DUAL BUS BUFFER

The KIC7W125FK is a high speed DUAL BUS BUFFER fabricated with silicon gate C²MOS technology. It achieve the high speed operation similar to equivalent LSTTL while maintaining the C²MOS low power dissipation. The require 3-state control input to be set high to place the output into the high impedance. All inputs are equipped with protection circuits against static discharge or transient excess voltage.

FEATURES



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