Unit: mm

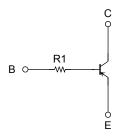
TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT process) (Bias Resistor Built-in Transistor)

# RN2112ACT,RN2113ACT

Switching Applications
Inverter Circuit Applications
Interface Circuit Applications
Driver Circuit Applications

- Extra small package (CST3) is applicable for extra high density fabrication.
- Incorporating a bias resistor into a transistor reduces parts count.
   Reducing the parts count enables the manufacture of ever more compact equipment and lowers assembly cost.
- Complementary to RN1112CT, RN1113CT

#### **Equivalent Circuit**



### Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	$V_{CBO}$	-50	V
Collector-emitter voltage	$V_{CEO}$	-50	٧
Emitter-base voltage	$V_{EBO}$	-5	٧
Collector current	IC	-80	mA
Collector power dissipation	PC	100 *	mW
Junction temperature	Tj	150	°C
Storage temperature range	T <sub>stg</sub>	-55 to 150	°C

<sup>\* :</sup> Mounted on FR4 board (10 mm × 10 mm × 1 mmt)

1.BASE
2.EMITTER
3.COLLECOTR

JEDEC

JEITA

TOSHIBA

2-1J1A

Weight: 0.75 mg (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e.operatingtemperature/current/voltage, etc.) are within the absolute maximum ratings.

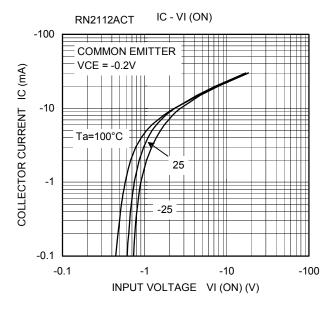
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

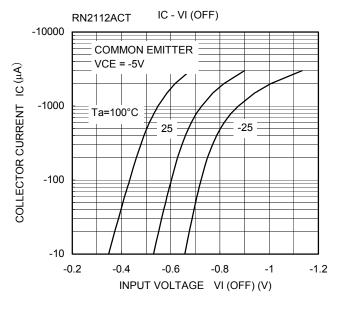


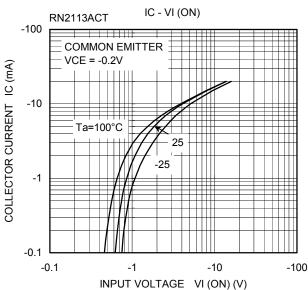
## **Electrical Characteristics (Ta = 25°C)**

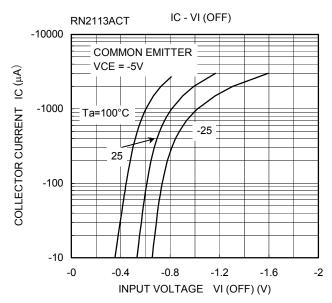
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current		I <sub>CBO</sub>	$V_{CB} = -50 \text{ V}, I_E = 0$	_	_	-100	nA
Emitter cut-off current	t	I <sub>EBO</sub>	$V_{EB} = -5 \text{ V}, I_{C} = 0$	_	_	-100	nA
DC current gain		h <sub>FE</sub>	$V_{CE} = -5 \text{ V}, I_{C} = -1 \text{ mA}$	120	_	400	
Collector-emitter saturation voltage		V <sub>CE (sat)</sub>	$I_C = -5 \text{ mA}, I_B = -0.25 \text{ mA}$			-0.15	<b>V</b>
Collector output capa	citance	C <sub>ob</sub>	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		0.9	_	pF
Input resistor	RN2112ACT	- R1	_	17.6	22	26.4	kΩ
	RN2113ACT			37.6	47	56.4	

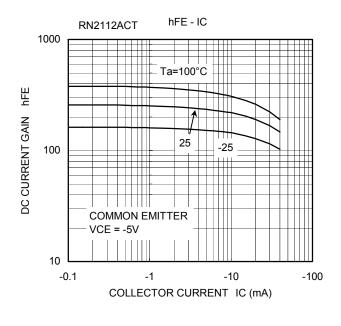
2

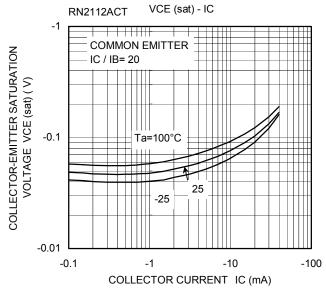


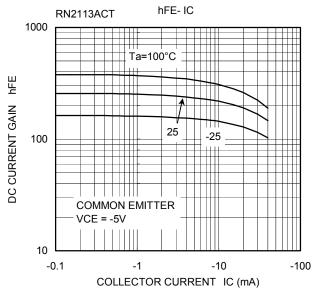


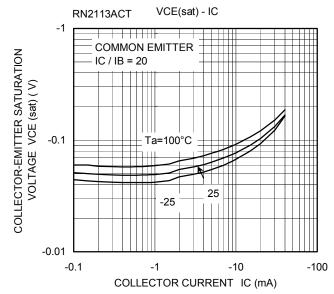












Type Name	Marking
RN2112ACT	Type Name  1 DH 3
RN2113ACT	Type Name  1 DJ 3

5

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