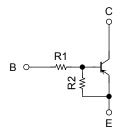
TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT process) (Bias Resistor built-in Transistor)

# RN2101CT,RN2102CT,RN2103CT RN2104CT,RN2105CT,RN2106CT

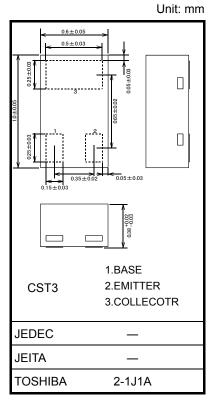
Switching Applications
Inverter Circuit Applications
Interface Circuit Applications
Driver Circuit Applications

- Incorporating a bias resistor into a transistor reduces parts count.
   Reducing the parts count enable the manufacture of ever more compact equipment and save assembly cost.
- Complementary to RN1101CT to RN1106CT

#### **Equivalent Circuit and Bias Resistor Values**



Type No.	R1 (kΩ)	R2 (kΩ)
RN2101CT	4.7	4.7
RN2102CT	10	10
RN2103CT	22	22
RN2104CT	47	47
RN2105CT	2.2	47
RN2106CT	4.7	47



Weight: 0.75 mg (typ.)

### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Collector-base voltage	RN2101CT to 2106CT	V <sub>CBO</sub>	-20	V	
Collector-emitter voltage	1(1021010110210001	$V_{CEO}$	-20	V	
Emitter-base voltage	RN2101CT to 2104CT	\/	-10	V	
	RN2105CT, 2106CT	V <sub>EBO</sub>	-5		
Collector current		IC	-50	mA	
Collector power dissipation	RN2101CT to 2106CT	PC	50	mW	
Junction temperature	KINZTOTCT to 2100CT	Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	

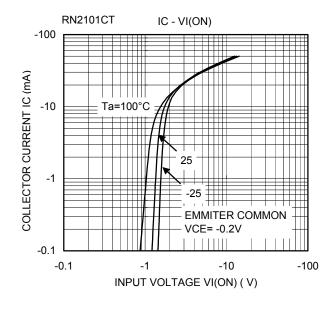
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e.operatingtemperature/current/voltage, etc.) are within the absolute maximum ratings.

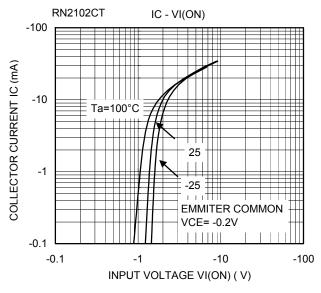
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

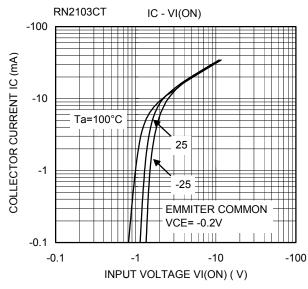


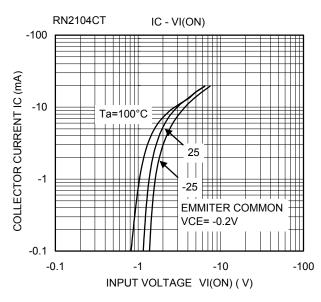
# Electrical Characteristics (Ta = 25°C)

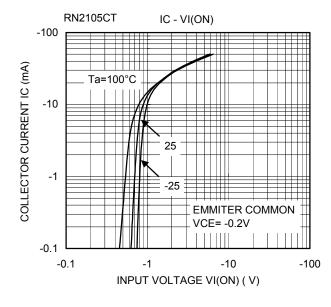
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	RN2101CT to 2106CT	I <sub>CBO</sub>	$V_{CB} = -20 \text{ V}, I_E = 0$		_	-100	- nA
	14421010110210001	I <sub>CEO</sub>	$V_{CE} = -20 \text{ V}, I_B = 0$		_	-500	
Emitter cut-off current	RN2101CT	l <sub>EBO</sub>	$V_{EB} = -10 \text{ V, } I_{C} = 0$	-0.89	_	-1.33	mA
	RN2102CT			-0.41	_	-0.63	
	RN2103CT			-0.18	_	-0.29	
	RN2104CT			-0.088	_	-0.133	
	RN2105CT		V <sub>FB</sub> = -5 V, I <sub>C</sub> = 0	-0.085	_	-0.127	
	RN2106CT		vEB = -3 v, IC = 0	-0.08	_	-0.121	
	RN2101CT			30	_	_	
	RN2102CT			60	_	_	
DC aurrent agin	RN2103CT	h	V <sub>CE</sub> = -5 V,	100	_	_	
DC current gain	RN2104CT	h <sub>FE</sub>	$I_C = -10 \text{ mA}$	120	_	_	
	RN2105CT	_		120	_	_	
	RN2106CT			120	_	_	
Collector-emitter saturation voltage	RN2101CT to 2106CT	V <sub>CE</sub> (sat)	$I_C = -5 \text{ mA},$ $I_B = -0.25 \text{ mA}$	_	_	-0.15	٧
	RN2101CT	Vi (on)	$V_{CE} = -0.2 \text{ V},$ $I_{C} = -5 \text{ mA}$	-1.0	_	-2.0	V
	RN2102CT			-1.0	_	-2.2	
	RN2103CT			-1.1	_	-2.7	
Input voltage (ON)	RN2104CT			-1.2	_	-3.6	
	RN2105CT			-0.6	_	-1.1	
	RN2106CT			-0.6	_	-1.2	
Innut valtage (OFF)	RN2101CT to 2104CT	V <sub>I (OFF)</sub>	$V_{CE} = -5 \text{ V},$ $I_{C} = -0.1 \text{ mA}$	-0.8	_	-1.5	٧
Input voltage (OFF)	RN2105CT, 2106CT			-0.4	_	-0.8	
Collector output capacitance	RN2101CT to 2106CT	C <sub>ob</sub>	$V_{CB} = -10 \text{ V}, I_E = 0,$ f = 1 MHz	_	1.2	_	pF
Input resistor	RN2101CT	R1	_	3.76	4.7	5.64	kΩ
	RN2102CT			8	10	12	
	RN2103CT			17.6	22	26.4	
	RN2104CT			37.6	47	56.4	
	RN2105CT			1.76	2.2	2.64	
	RN2106CT			3.76	4.7	5.64	
Resistor ratio	RN2101CT to 2104CT	R1/R2	_	0.8	1.0	1.2	
	RN2105CT			0.0376	0.0468	0.0562	
	RN2106CT			0.08	0.1	0.12	

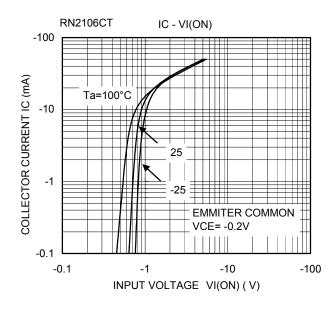


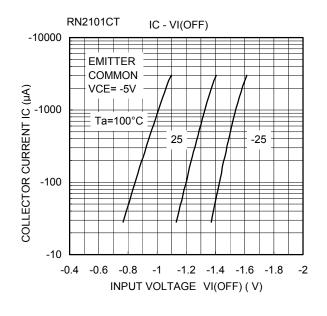


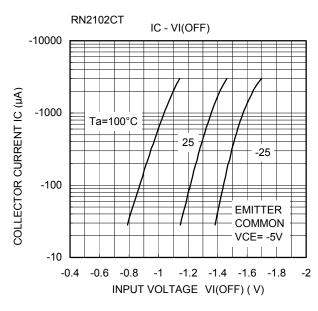


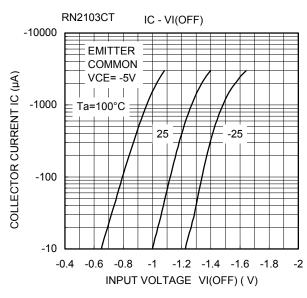


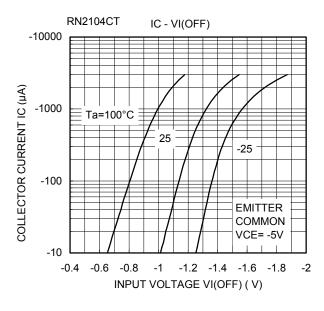


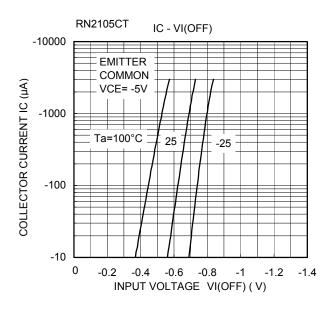


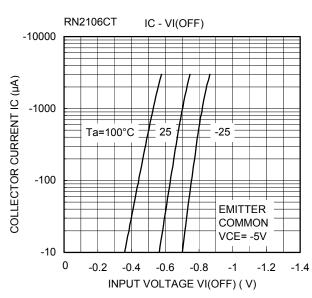


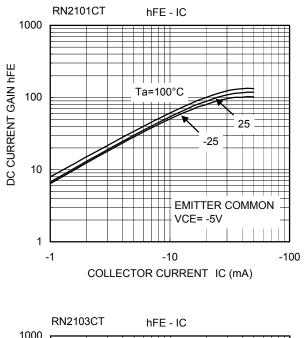


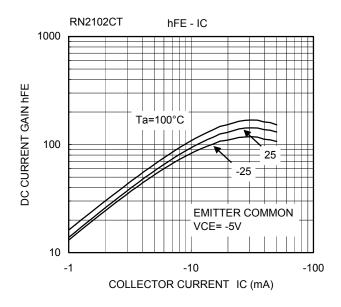


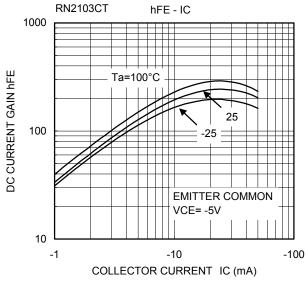


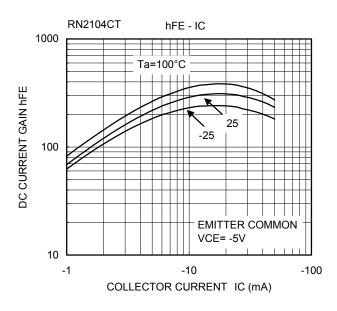


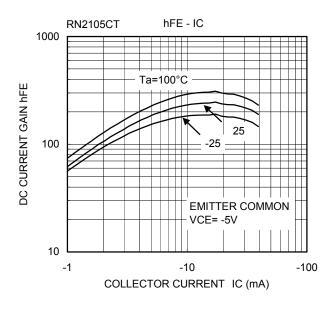


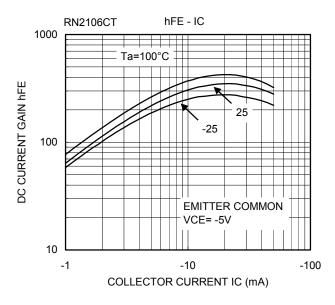


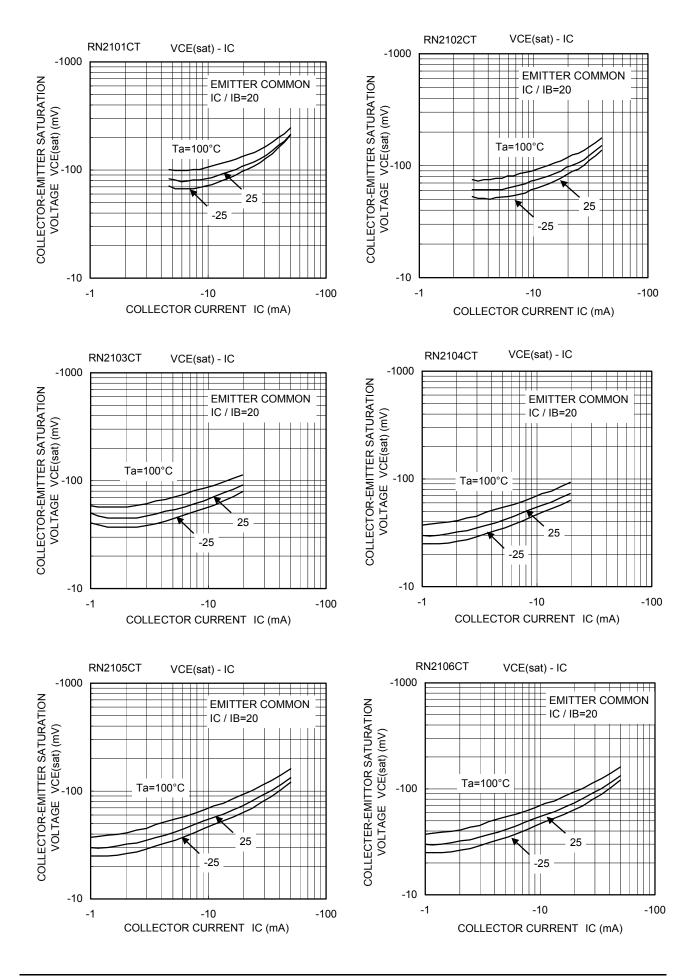












Type Name	Marking
RN2101CT	Type name  1 2  U0  3
RN2102CT	Type name  1  2  U1  3
RN2103CT	Type name  1  2  1  2
RN2104CT	Type name  1 2  U3  3
RN2105CT	Type name  1 2
RN2106CT	Type name  1  2  U5  3

## **Handling Precaution**

When handling individual devices (which are not yet mounted on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

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