Unit: mm

TOSHIBA Field Effect Transistor Silicon P, N Channel MOS Type (U-MOS IV / U-MOS III)

# **TPCF8402**

# Portable Equipment Applications Motor Drive Applications DC-DC Converter Applications

Low drain-source ON resistance : P Channel R<sub>DS</sub> (ON) = 60 m $\Omega$  (typ.)

N Channel R<sub>DS</sub> (ON) = 38 m $\Omega$  (typ.)

High forward transfer admittance : P Channel  $|Y_{fs}| = 5.9 \text{ S}$  (typ.)

N Channel  $|Y_{fs}| = 6.8 \text{ S (typ.)}$ 

Low leakage current : P Channel  $I_{DSS} = -10 \mu A (V_{DS} = -30 \text{ V})$ 

N Channel  $I_{DSS} = 10 \mu A (V_{DS} = 30 V)$ 

Enhancement-mode

: P Channel  $V_{th} = -0.8$  to  $-2.0 \text{ V} (V_{DS} = -10 \text{ V}, I_D = -1 \text{mA})$ 

N Channel  $V_{th} = 1.3 \text{ to } 2.5 \text{ V } (V_{DS} = 10 \text{ V}, I_D = 1 \text{mA})$ 

### Absolute Maximum Ratings (Ta = 25°C)

Cł	Symbol	Rating		Unit		
Drain-source voltage		$V_{DSS}$	-30	30	V	
Drain-gate volt	tage ( $R_{GS} = 20 \text{ k}\Omega$ )	$V_{DGR}$	-30	30	V	
Gate-source v	oltage	V <sub>GSS</sub>	±20	±20	V	
Drain current	DC (Note 1)	ID	-3.2	4.0	А	
Diam current	Pulse (Note 1)	$I_{DP}$	-12.8	16.0	^	
Drain power dissipation	Single-device operation (Note 3a)	P <sub>D (1)</sub>	1.35	1.35		
(t = 5 s) (Note 2a)	Single-device value at dual operation (Note 3b)	P <sub>D (2)</sub>	1.12	1.12	W	
Drain power dissipation (t = 5 s) (Note 2b)	Single-device operation (Note 3a)	P <sub>D (1)</sub>	0.53	0.53		
	Single-device value at dual operation (Note 3b)	P <sub>D (2)</sub>	0.33	0.33		
Single pulse a	valanche energy (Note 4)	E <sub>AS</sub>	0.67	2.6	mJ	
Avalanche cur	rent	I <sub>AR</sub>	-1.6	2.0	Α	
Repetitive avalanche energy Single-device value at dual operation (Note 2a, 3b, 5)		E <sub>AR</sub>	0.11		mJ	
Channel tempe	Channel temperature		150		°C	
Storage tempe	erature range	T <sub>stg</sub>	-55 to 150		°C	

 $2.9 \pm 0.1$ 0.3 +0.1/-0.05 0.025 M A  $0.8 \pm 0.05$ -0.05|S Α 0.71+0.04 0.24 +0.10 10.04 Source 1 Drain 2
 Drain 1 2. Gate 1

Weight: 0.011 g (typ.)

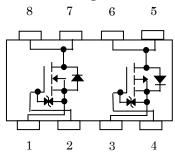
**JEDEC JEITA TOSHIBA** 

### **Circuit Configuration**

3. Source 2 4. Gate 2

8. Drain 1

2-3U1B



Note: For Notes 1 to 5, refer to the next page.

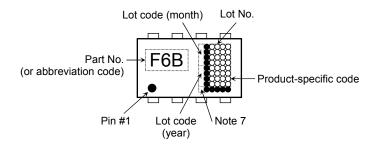
Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with caution.

#### **Thermal Characteristics**

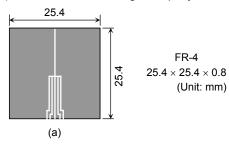
Charac	Symbol	Max	Unit		
Thermal resistance, channel to ambient (t = 5 s) (Note 2a)	Single-device operation (Note 3a)	R <sub>th (ch-a) (1)</sub>	92.6	°C/W	
	Single-device value at dual operation (Note 3b)	R <sub>th (ch-a) (2)</sub>	111.6		
Thermal resistance, channel to ambient	Single-device operation (Note 3a)	R <sub>th (ch-a) (1)</sub>	235.8	°C/W	
(t = 5 s) (Note 2b)	Single-device value at dual operation (Note 3b)	R <sub>th (ch-a) (2)</sub>	378.8	C/ <b>VV</b>	

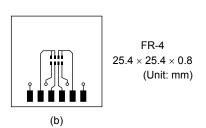
## Marking (Note 6)



Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (b) Device mounted on a glass-epoxy board (b)





Note 3: a) The power dissipation and thermal resistance values are shown for a single device. (During single-device operation, power is only applied to one device.)

b) The power dissipation and thermal resistance values are shown for a single device. (During dual operation, power is evenly applied to both devices.)

Note 4: P Channel:  $V_{DD}=-24$  V,  $T_{ch}=25^{\circ}$ C (initial), L=0.2 mH,  $R_{G}=25$   $\Omega$ ,  $I_{AR}=-1.6$  A N Channel:  $V_{DD}=24$  V,  $T_{ch}=25^{\circ}$ C (initial), L=0.5 mH,  $R_{G}=25$   $\Omega$ ,  $I_{AR}=2.0$  A

Note 5: Repetitive rating: Pulse width limited by maximum channel temperature.

Note 6: "●" on the lower left of the marking indicates Pin 1.

Note 7 A dot marking identifies the indication of product Labels.

Without a dot: [[Pb]]/INCLUDES > MCV

With a dot: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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# **Electrical Characteristics (Ta = 25°C)**

Cha	aracteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Drain cut-off curre	ent	I <sub>DSS</sub>	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	-10	μΑ
Drain-source bre	akdown voltage	V <sub>(BR) DSS</sub>	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$ -30	_	_	V	
Drain-source breakdown voltage		V <sub>(BR) DSX</sub>	$I_D = -10 \text{ mA}, V_{GS} = 20 \text{ V}$	-15	_	_	v
Gate threshold vo	oltage	V <sub>th</sub>	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$	-0.8	_	-2.0	٧
Drain-source ON	resistance	Pro (ON)	$V_{GS} = -4.5 \text{ V}, I_D = -1.6 \text{A}$	_	80	105	mΩ
Dialii-source ON	resistance	R <sub>DS</sub> (ON)	$V_{GS} = -10 \text{ V}, I_D = -1.6 \text{ A}$	_	60	72	11152
Forward transfer	admittance	Y <sub>fs</sub>	$V_{DS} = -10 \text{ V}, I_D = -1.6 \text{ A}$	2.9	5.9	_	S
Input capacitance	9	C <sub>iss</sub>	V <sub>DS</sub> = -10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	600	_	pF
Reverse transfer	capacitance	C <sub>rss</sub>		_	60	_	
Output capacitance		C <sub>oss</sub>		_	70	_	
	Rise time	t <sub>r</sub>	V <sub>GS</sub> = -1.6 A	_	5.3	_	
Switching time	Turn-on time	t <sub>on</sub>		_	12	_	
Switching time	Fall time	t <sub>f</sub>		_	8.4	_	ns
	Turn-off time	t <sub>off</sub>	$V_{DD} \approx -15 \text{ V}$ Duty $\leq$ 1%, $t_W =$ 10 $\mu s$	_	34	_	
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \approx -24 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -3.2 \text{ A}$	_	14	_	
Gate-source charge 1		Q <sub>gs1</sub>		_	1.4	_	nC
Gate-drain ("miller") charge		$Q_{gd}$		_	2.7	_	

# Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse (Note 1)	I <sub>DRP</sub>	_	_	_	-12.8	Α
Forward voltage (diode)		$V_{DSF}$	$I_{DR} = -3.2 \text{ A}, V_{GS} = 0 \text{ V}$	_		1.2	V

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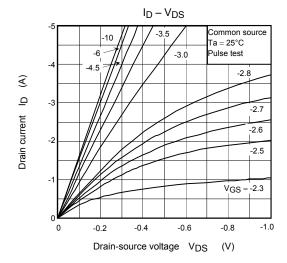


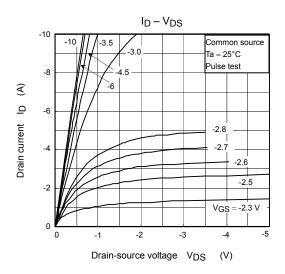
# Electrical Characteristics (Ta = 25°C)

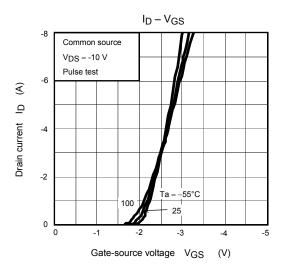
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V	_	_	±10	μA
Drain cut-off curre	ent	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V	_	_	10	μA
Drain-source brea	akdown	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	30	_	_	V
voltage		V <sub>(BR) DSX</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = -20 V	15	_	_	V
Gate threshold vo	oltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.3	_	2.5	V
Drain-source ON	registance	D	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 2.0 A	_	58	77	mΩ
Drain-source ON	resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 2.0 A	_	38	50	
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 2.0 A	3.4	6.8	_	S
Input capacitance	:	C <sub>iss</sub>		_	470	_	pF
Reverse transfer	capacitance	C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	60	_	
Output capacitan	ce	Coss		_	80	_	
Switching time	Rise time	t <sub>r</sub>	$V_{GS} = \frac{10 \text{ V}}{0 \text{ V}} = \frac{10 \text{ P}}{0 \text{ V}} = \frac{2.0 \text{ A}}{0 \text{ V}} = \frac{0 \text{ V}}{0 \text{ V}} = 0 \text{ $	_	5.2	_	
	Turn-on time	t <sub>on</sub>		_	8.3	_	
	Fall time	t <sub>f</sub>		_	4.0	_	ns
	Turn-off time	t <sub>off</sub>		_	22	_	
Total gate charge (gate-source plus gate-drain)		Qg		_	10	_	
Gate-source charge 1		Q <sub>gs1</sub>	$V_{DD} \approx 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 4 \text{ A}$	_	1.7	_	nC
Gate-drain ("miller") charge		Q <sub>gd</sub>			2.4	_	

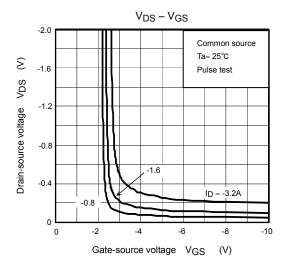
# Source-Drain Ratings and Characteristics (Ta = 25°C)

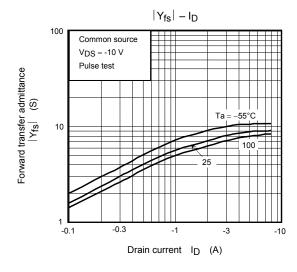
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse (Note 1)	I <sub>DRP</sub>	_	_	_	16.0	Α
Forward voltage (diode)		$V_{DSF}$	I <sub>DR</sub> = 4.0 A, V <sub>GS</sub> = 0 V		_	-1.2	V

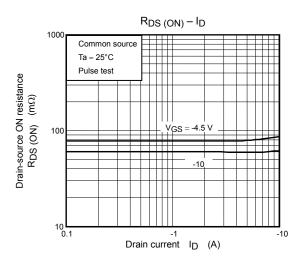


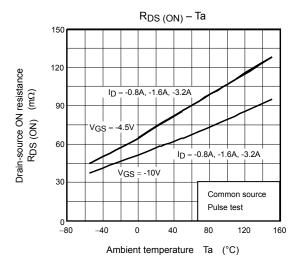


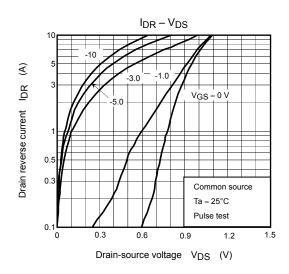


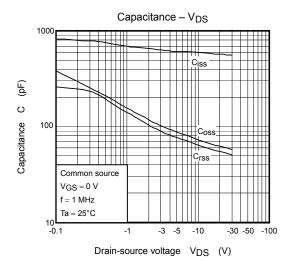


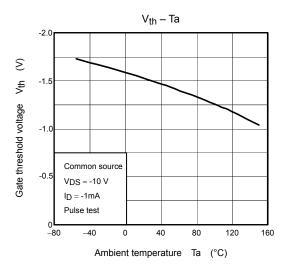


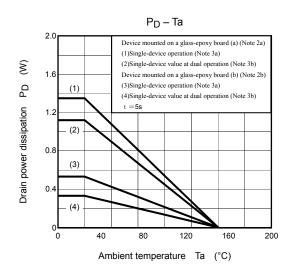


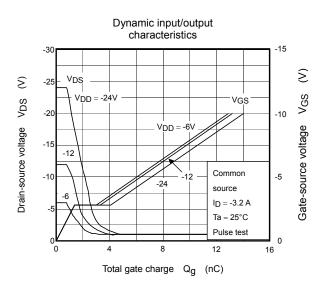


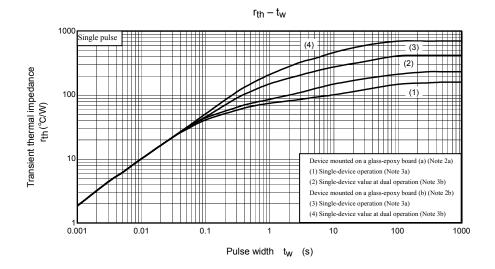


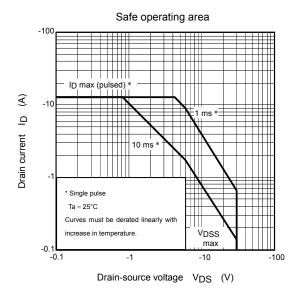


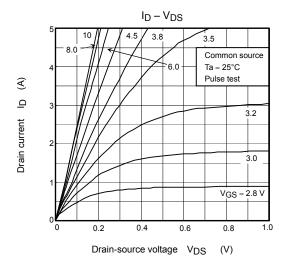


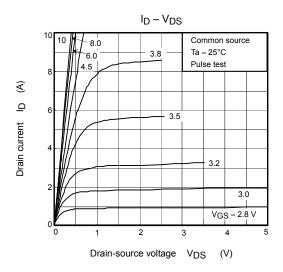


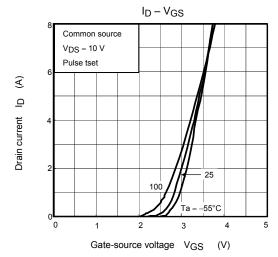


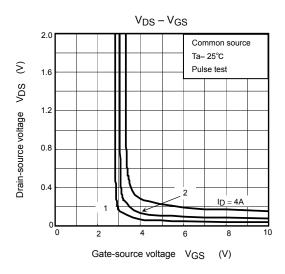


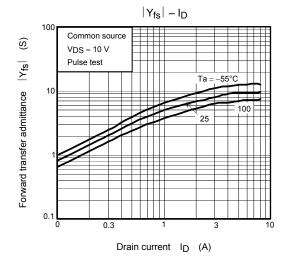


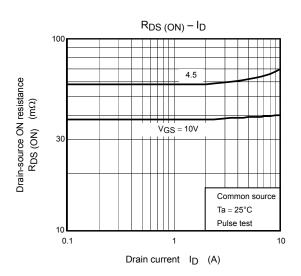


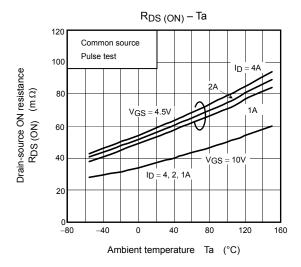


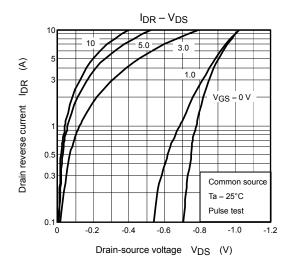


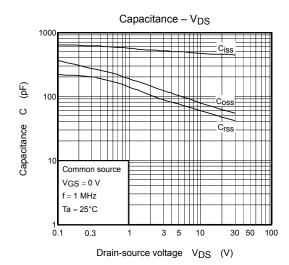


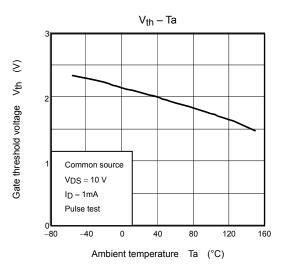


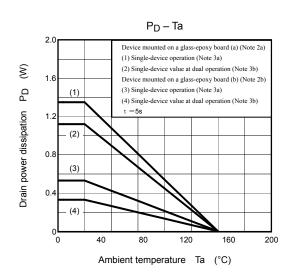


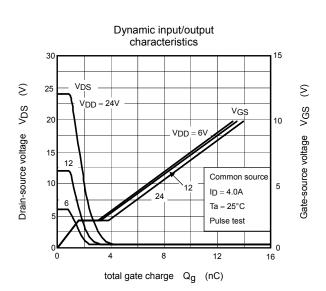


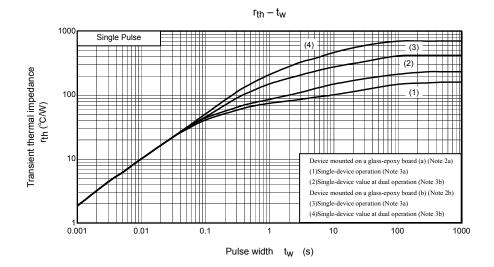


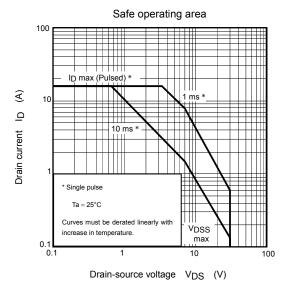












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