TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSVII)

# TK2Q60D

## **Switching Regulator Applications**

• Low drain-source ON-resistance: RDS (ON) =  $3.2 \Omega(\text{typ.})$ 

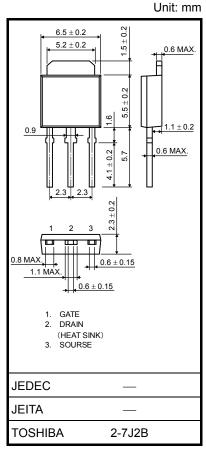
• High forward transfer admittance:  $|Y_{fs}| = 1.0 \text{ S (typ.)}$ 

• Low leakage current:  $I_{DSS} = 10 \mu A (max) (V_{DS} = 600 V)$ 

• Enhancement mode:  $V_{th} = 2.4 \text{ to } 4.4 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$ 

## Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	600	V	
Gate-source voltage		$V_{GSS}$	±30	V	
Drain current	DC (Note 1)	I <sub>D</sub>	2	Α	
	Pulse (Note 1)	I <sub>DP</sub>	8	A	
Drain power dissipati	on (Tc = 25°C)	$P_{D}$	60	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	101	mJ	
Avalanche current		I <sub>AR</sub>	2	Α	
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	6.0	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	



Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

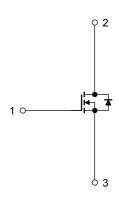
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	2.08	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	125	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD} = 90 \text{ V}$ ,  $T_{ch} = 25^{\circ}\text{C}(\text{initial})$ , L = 44.1 mH,  $R_G = 25\Omega$ ,  $I_{AR} = 2 \text{ A}$ 

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



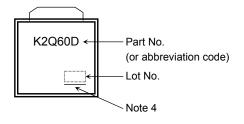
# **Electrical Characteristics (Ta = 25°C)**

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cui	rent	I <sub>GSS</sub>	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μΑ
Drain cut-off curr	ent	I <sub>DSS</sub>	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V	_	_	10	μА
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	600	_	_	V
Gate threshold ve	oltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.4	_	4.4	V
Drain-source ON	resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> =1 A	_	3.2	4.3	Ω
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> =1 A	0.3	1.0	_	S
Input capacitance		C <sub>iss</sub>		_	280	_	
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	1.5	_	pF
Output capacitance		C <sub>oss</sub>	]		30	_	
Switching time	Rise time	t <sub>r</sub>	$\begin{array}{c c} 10 \text{ V} \\ \text{VGS} \\ 0 \text{ V} \\ \hline \\ 50 \Omega \\ \end{array}$ $\begin{array}{c c} I_D = 1 \text{ A} & \text{V}_{\text{OUT}} \\ \hline \\ \hline \\ \\ \end{array}$ $\begin{array}{c c} R_L = 200 \Omega \\ \hline \\ \\ \end{array}$ $\begin{array}{c c} V_{\text{DD}} \approx 200 \text{ V} \\ \end{array}$		15	_	
	Turn-on time	t <sub>on</sub>			35	_	
	Fall time	t <sub>f</sub>		_	7	_	ns
	Turn-off time	t <sub>off</sub>	Duty ≤ 1%, t <sub>W</sub> = 10 μs	_	55	_	
Total gate charge		Qg		_	7	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 2 \text{ A}$	_	4	_	nC
Gate-drain charge		Q <sub>gd</sub>		_	3	_	

# Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	2	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	8	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 2 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = 2 \text{ A}, V_{GS} = 0 \text{ V},$	_	550	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> /dt = 100 A/μs	_	2.2	_	μС

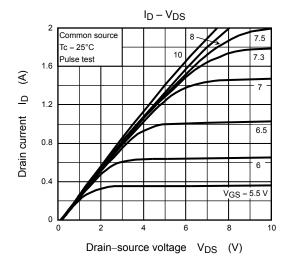
### Marking

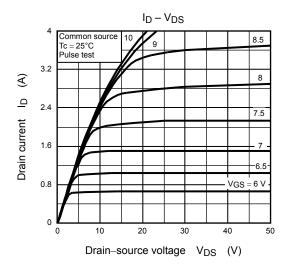


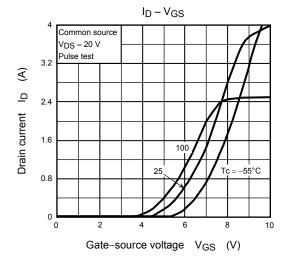
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

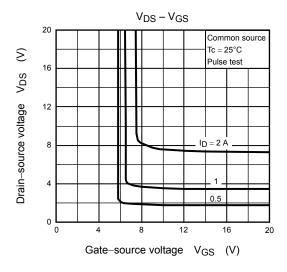
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

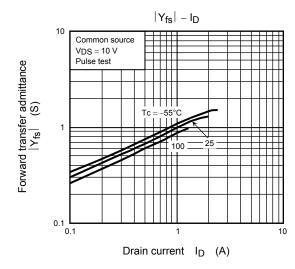
The RoHS is Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment

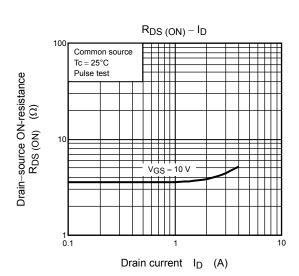


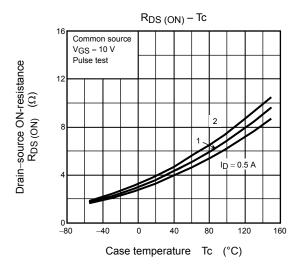


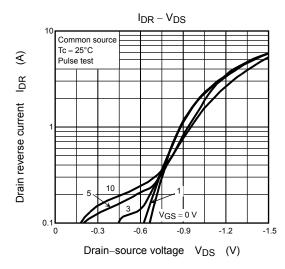


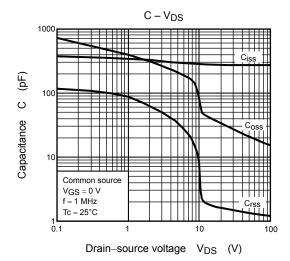


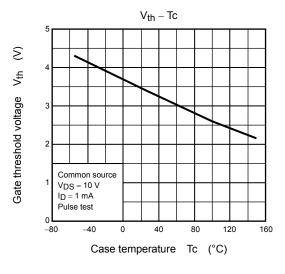


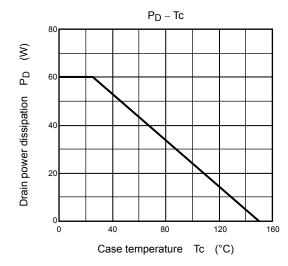


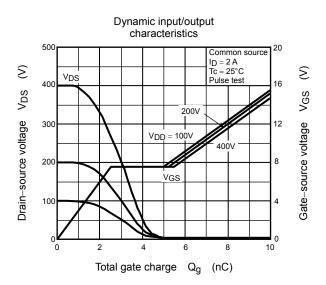




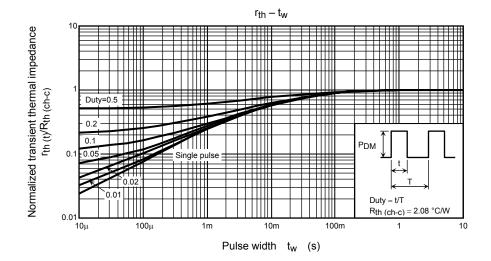


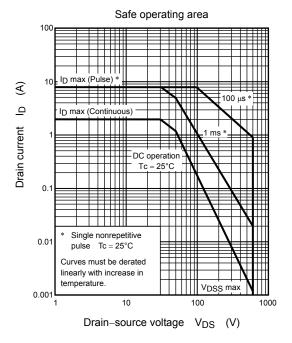


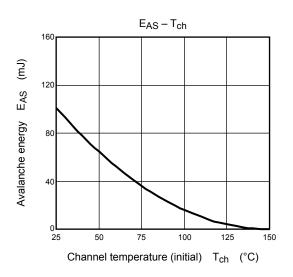


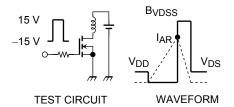


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$$\begin{aligned} R_G &= 25~\Omega \\ V_{DD} &= 90~V,~L = 44.1~mH \end{aligned} \qquad EAS &= \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{BVDSS}{BVDSS - V_{DD}} \right) \end{aligned}$$

5 2010-08-30

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