TOSHIBA Field Effect Transistor Silicon N Channel MOS Type $(\pi - MOSVII)$

TK10A50D

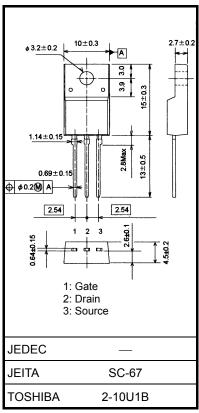
Switching Regulator Applications

Unit: mm

- Low drain-source ON-resistance: RDS (ON) = 0.62Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 5.0 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 500 \text{ V)}$
- Enhancement mode: $V_{th} = 2.0 \text{ to } 4.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA)}$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	500	V	
Gate-source voltage		V_{GSS}	±30	V	
Drain current	DC (Note 1)	I _D	10		
	Pulse (t = 1 ms) (Note 1)	I _{DP}	40	Α	
Drain power dissipati	on (Tc = 25°C)	PD	45	W	
Single pulse avalanche energy (Note 2)		E _{AS}	264	mJ	
Avalanche current		I _{AR}	10	Α	
Repetitive avalanche energy (Note 3)		E _{AR}	4.5	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	2.78	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

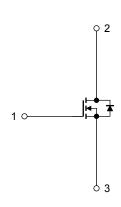
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 4.49 mH, R_G = 25 Ω , I_{AR} = 10 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

Internal Connection



Electrical Characteristics (Ta = 25°C)

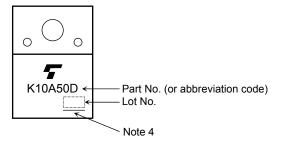
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μА
Drain cut-off current		I _{DSS}	V _{DS} = 500 V, V _{GS} = 0 V		_	10	μА
Drain-source breakdown voltage		V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	500	_	_	٧
Gate threshold voltage		V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	٧
Drain-source ON	-resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 5 A		0.62	0.72	Ω
Forward transfer admittance Yfs		Y _{fs}	V _{DS} = 10 V, I _D = 5 A	1.3	5.0	_	S
Input capacitance		C _{iss}		_	1050	_	pF
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz		5	_	
Output capacitance		C _{oss}			100	_	
Switching time	Rise time	t _r	$\begin{array}{c c} 10 \text{ V} & \text{I}_D = 5 \text{ A} & \text{V}_{\text{OUT}} \\ \hline V_{\text{GS}} & \text{V} & \text{RL} = 40 \Omega \end{array}$	_	25		. ns
	Turn-on time	t _{on}		_	60	_	
	Fall time	t _f			10		
	Turn-off time	t _{off}	Duty ≤ 1%, t _W = 10 μs		75	_	
Total gate charge		Qg		_	20	_	
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$	_	13	_	nC
Gate-drain charge		Q _{gd}]	_	7	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	10	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	40	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 10 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	$I_{DR} = 10 \text{ A}, V_{GS} = 0 \text{ V},$	_	1300	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} /dt = 100 A/μs	_	12	_	μС

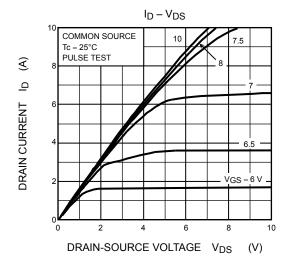
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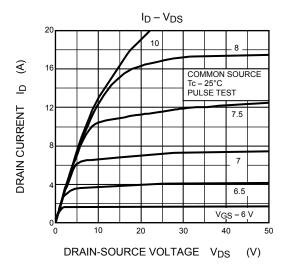
Marking

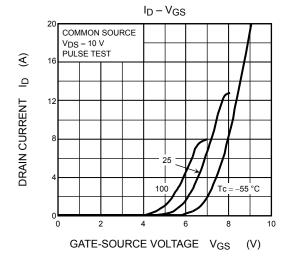


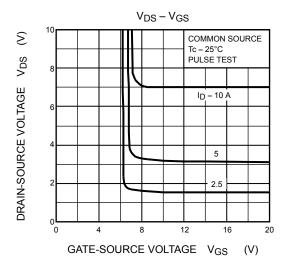
Note 4: A line under a Lot No. identifies the indication of product Labels.: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

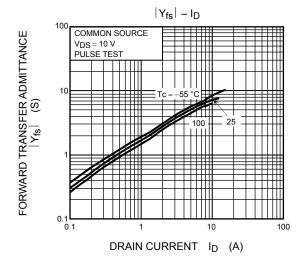
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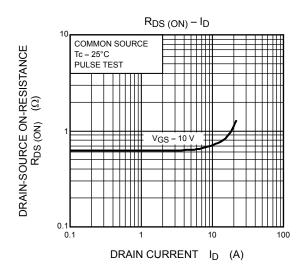


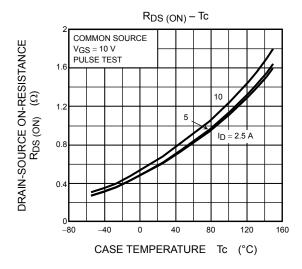


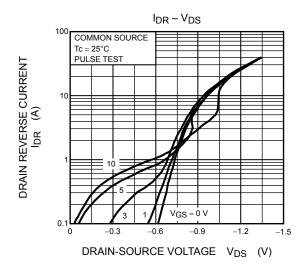


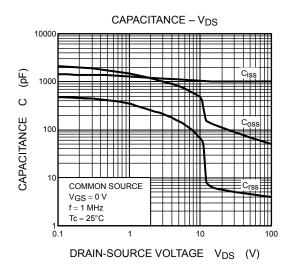


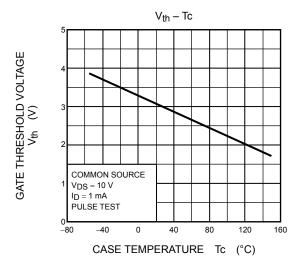


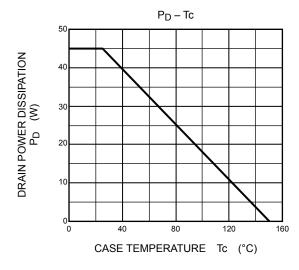


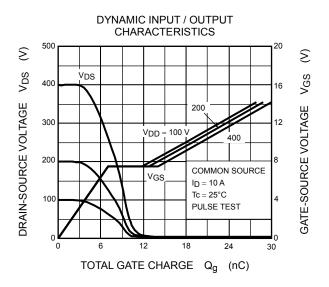




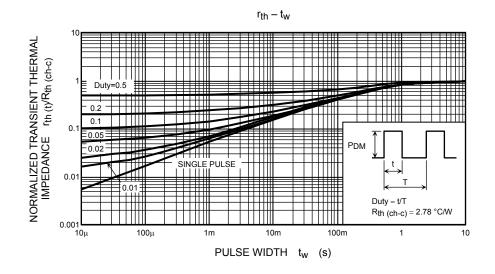


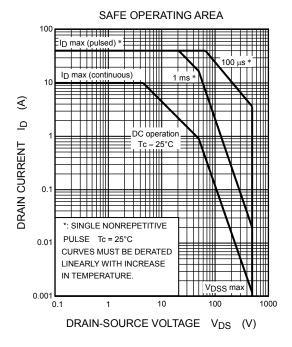


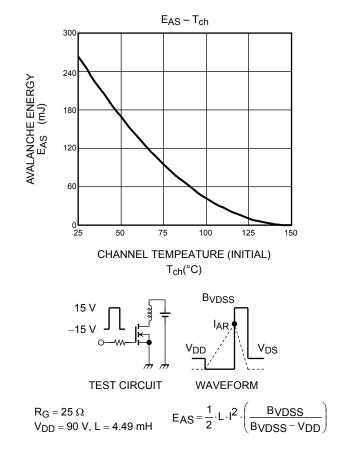




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