TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74HC04AP,TC74HC04AF

Hex Inverter

The TC74HC04A is a high speed CMOS INVERTER fabricated with silicon gate C^2 MOS technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

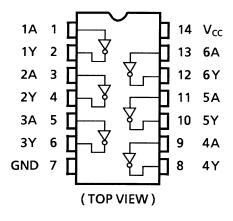
The internal circuit is composed of 3 stages, including buffered output, which provide high noise immunity and stable output.

All inputs are equipped with protection circuits against satic discharge or transient excess voltage.

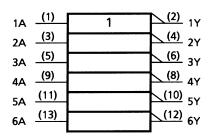
Features

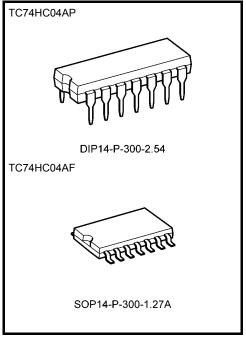
- High speed: $t_{pd} = 6$ ns (typ.) at $V_{CC} = 5$ V
- Low power dissipation: $I_{CC} = 1 \mu A \text{ (max)}$ at $T_{a} = 25 \text{°C}$
- High noise immunity: $V_{NIH} = V_{NIL} = 28\% V_{CC}$ (min)
- Output drive capability: 10 LSTTL loads
- Symmetrical output impedance: $|I_{OH}| = I_{OL} = 4 \text{ mA (min)}$
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: V_{CC} (opr) = 2 to 6 V
- Pin and function compatible with 74LS04

Pin Assignment



IEC Logic Symbol





Weight

DIP14-P-300-2.54 : 0.96 g (typ.) SOP14-P-300-1.27A : 0.18 g (typ.)

Truth Table

Α	Υ
L	Н
Н	L

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit	
Supply voltage range	V_{CC}	–0.5 to 7	V	
DC input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V	
DC output voltage	V _{OUT}	−0.5 to V _{CC} + 0.5	V	
Input diode current	I _{IK}	±20	mA	
Output diode current	lok	±20	mA	
DC output current	lout	±25	mA	
DC V _{CC} /ground current	Icc	±50	mA	
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW	
Storage temperature	T _{stg}	-65 to 150	°C	

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65° C. From Ta = 65 to 85° C a derating factor of -10 mW/°C shall be applied until 300 mW.

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2 to 6	V
Input voltage	V _{IN}	0 to V _{CC}	V
Output voltage	Vout	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C
		0 to 1000 (V _{CC} = 2.0 V)	
Input rise and fall time	t _r , t _f	0 to 500 (V _{CC} = 4.5 V)	ns
		0 to 400 (V _{CC} = 6.0 V)	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition			-	Γa = 25°C		_	a = o 85°C	Unit
		\		V _{CC} (V)	Min	Тур.	Max	Min	Max	
				2.0	1.50	_	_	1.50	_	
High-level input voltage	V_{IH}		_	4.5	3.15	_	_	3.15	_	V
ŭ				6.0	4.20	_	_	4.20	_	
				2.0	_	_	0.50	_	0.50	
Low-level input voltage	V_{IL}	_		4.5	_	_	1.35	_	1.35	V
1 11 9 1				6.0		_	1.80	_	1.80	
		V _{IN} = V _{IH} or V _{IL} .		2.0	1.9	2.0	_	1.9	_	
			$I_{OH} = -20 \mu A$	4.5	4.4	4.5	_	4.4	_	
High-level output voltage	V _{OH}			6.0	5.9	6.0	_	5.9	_	V
			$I_{OH} = -4 \text{ mA}$	4.5	4.18	4.31	_	4.13	_	
			$I_{OH} = -5.2 \text{ mA}$	6.0	5.68	5.80	_	5.63	_	
		V _{OL} V _{IN} = V _{IH} or V _{IL}		2.0	_	0.0	0.1	_	0.1	
			$I_{OL} = 20 \mu A$	4.5	_	0.0	0.1		0.1	
Low-level output voltage VoL	V _{OL}			6.0		0.0	0.1	_	0.1	V
			$I_{OL} = 4 \text{ mA}$	4.5	_	0.17	0.26	_	0.33	
			I _{OL} = 5.2 mA	6.0		0.18	0.26	_	0.33	
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND		6.0	_		±0.1		±1.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or	GND	6.0	_	_	1.0	_	10.0	μА

AC Characteristics ($C_L = 15 \text{ pF}$, $V_{CC} = 5 \text{ V}$, $Ta = 25^{\circ}\text{C}$, input: $t_r = t_f = 6 \text{ ns}$)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Output transition time	tTLH	_		4	8	ns
	t _{THL}					
Propagation delay time	t _{pLH}	_	_	6	12	ns
	t _{pHL}					

3



AC Characteristics ($C_L = 50 \text{ pF}$, input: $t_r = t_f = 6 \text{ ns}$)

Characteristics Symbol		Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
			V _{CC} (V)	Min	Тур.	Max	Min	Max	
	4 —		2.0	_	30	75	_	95	
Output transition time	t _{TLH}	_	4.5	_	8	15	_	19	ns
	t _{THL}		6.0	_	7	13	_	16	
	4		2.0	_	27	75	_	95	
Propagation delay time	t _{pLH}	_	4.5	_	9	15	_	19	ns
	t _{pHL}		6.0	_	8	13	_	16	
Input capacitance	C _{IN}	_		_	5	10	_	10	pF
Power dissipation CPE	C _{PD}				20				٠,٢
capacitance	(Note)				20				pF

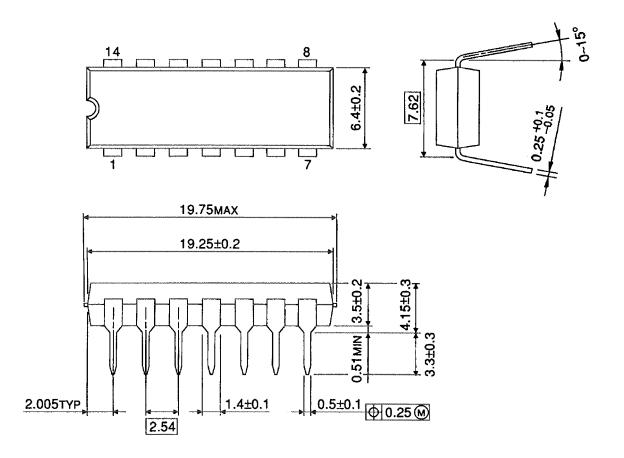
Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

$$I_{CC}$$
 (opr) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/6$ (per gate)

Package Dimensions

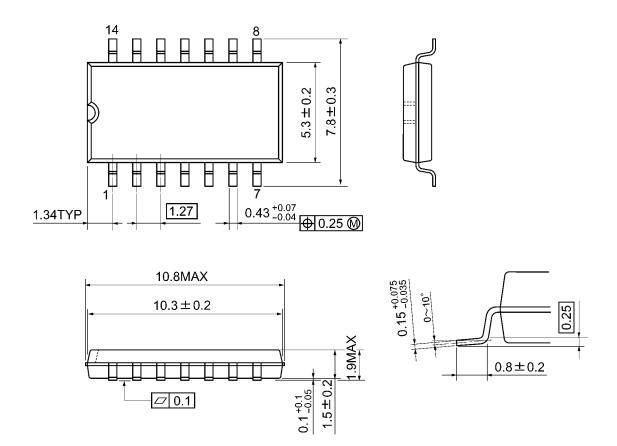
DIP14-P-300-2.54 Unit: mm



Weight: 0.96 g (typ.)

Package Dimensions

SOP14-P-300-1.27A Unit: mm



Weight: 0.18 g (typ.)

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