TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC4066BP,TC4066BF,TC4066BFT

TC4066B Quad Bilateral Switch

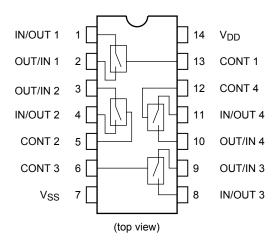
TC4066B contains four independent circuits of bidirectional switches. When control input CONT is set to "H" level, the impedance between input and output of the switch becomes low and when it is set to "L" level, the impedance becomes high. This can be applied for switching of analog signals and digital signals.

• ON-resistance, Ron

250 Ω (typ.): $V_{DD} - V_{SS} = 5 \text{ V}$ 110 Ω (typ.): $V_{DD} - V_{SS} = 10 \text{ V}$ 70 Ω (typ.): $V_{DD} - V_{SS} = 15 \text{ V}$

• OFF-resistance, Roff Roff (typ.) $> 10^9 \Omega$

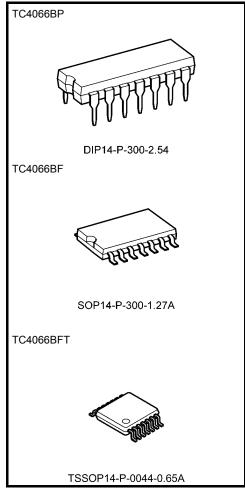
Pin Assignment



Truth Table

Control	Impedance between IN/OUT-OUT/IN	(Note)
Н	0.5 to $5 \times 10^2 \Omega$	
L	>10 ⁹ Ω	

Note: See static electrical characteristics



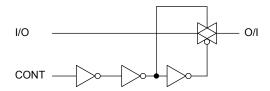
Weight

DIP14-P-300-2.54 : 0.96 g (typ.) SOP14-P-300-1.27A : 0.18 g (typ.) TSSOP14-P-0044-0.65A : 0.06 g (typ.)



Logic Diagram

1/4 TC4066B



Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
DC supply voltage	V_{DD}	V_{SS} – 0.5 to V_{SS} + 20	V
Control input voltage	V _{CIN}	V _{SS} – 0.5 to V _{DD} + 0.5	V
Switch I/O voltage	V _{I/O}	V _{SS} – 0.5 to V _{DD} + 0.5	V
Potential difference across I/O during ON	I _{I/O}	±0.5	V
Control input current	I _{CIN}	±10	mA
Power dissipation	PD	300 (DIP)/180 (SOIC)	mW
Operating temperature range	T _{opr}	-40 to 85	°C
Storage temperature range	T _{stg}	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (V_{SS} = 0 V) (Note)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
DC supply voltage	V_{DD}	_	3	_	18	V	
Input voltage	V _{DD} /V _{OUT}	_	0	_	V_{DD}	V	

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Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused control inputs must be tied to either V_{DD} or V_{SS} .

Static Electrical Characteristics (in case not specifically appointed, $V_{SS} = 0 V$)

Characteristics			Test Condition		-40°C		25°C			85°C		
		Symbol		V _{DD} (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit
				5	3.5	_	3.5	2.75	_	3.5	_	
Control in high volt		V_{IH}	I _{IS} < 10 μA	10	7.0	_	7.0	5.50	_	7.0	_	V
	_			15	11.0	_	11.0	8.25	_	11.0	_	
				5	_	1.5	_	2.25	1.5	_	1.5	
Control i		V_{IL}	I _{IS} < 10 μA	10	_	3.0	_	4.50	3.0	_	3.0	V
	_			15	_	4.0	_	6.75	4.0	_	4.0	
			$0 \le V_{IS} \le VDD$	5	_	800	_	290	950	_	1200	
On-state resistan		R _{ON}	$R_L = 10 \text{ k}\Omega$	10	_	210	_	120	250	_	300	Ω
				15	_	140	_	85	160	_	200	
ΔOn-state resistance (between any		R _{ON} ∆	_	5	_	_	_	10	_	_	_	
				10	_	_	_	6	_	_	_	Ω
2 switch				15	_	_	_	4	_	_	_	
Input/ou			V _{IN} = 18 V, V _{OUT} = 0 V	18	_	±100	_	±0.1	±100	_	±1000	^
leakage current		l _{OFF}	V _{IN} = 0 V, V _{OUT} = 18 V	18	_	±100	_	±0.1	±100	_	±1000	nA
			., ., .,	5	_	0.25	_	0.001	0.25	_	7.5	
Quiescent supply current		nt I _{DD}	$V_{IN} = V_{SS}, V_{DD}$ (Note)	10	_	0.50	_	0.001	0.50	_	15.0	μΑ
				15	_	1.00	_	0.002	1.00	_	30.0	
Input	"H" level	l _{IH}	V _{IH} = 18 V	18	_	0.1		10 ⁻⁵	0.1		1.0	μА
current	"L" level	IIL	V _{IL} = 0 V	18	_	-0.1		-10 ⁻⁵	-0.1		-1.0	μΑ

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Note: All valid input combinations.



Dynamic Electrical Characteristics (Ta = 25°C, $V_{SS} = 0 \text{ V}$, $C_L = 50 \text{ pF}$)

		Test Condition							
Characteristics	Symbol		$egin{array}{c c} V_{SS} & V_{DD} \ \hline (V) & (V) \end{array}$			Min	Тур.	Max	Unit
				0	5	_	15	40	
Phase difference between input to output	фІ-О	C _L = 50 pF		0	10	_	8	20	ns
				0	15	—	5	15	
Propagation delay time	t _{pZL}	$R_L = 1 k\Omega$		0	5	_	55	120	
(control-OUT)	t _{pZH}	$C_L = 50 \text{ pF}$		0	10	_	25	40	ns
(60111101-001)	фин	OL = 30 βi		0	15	_	20	30	
Propagation delay time	t _{pLZ}	$R_L = 1 k\Omega$		0	5	_	45	80	
(control -OUT)	t _{pHZ}	$C_L = 50 \text{ pF}$		0	10	_	30	70	ns
(donator do 1)	φπΖ	ο σο μι		0	15	_	25	60	
	f _{max} (C)	$R_L = 1 k\Omega$		0	5	_	10	_	
Max control input repetition rate		$C_L = 50 \text{ pF}$		0	10	_	12	_	MHz
		OL = 30 βi		0	15	_	12	_	
-3dB cutoff frequency	f _{max} (I-O)	$R_L = 1 k\Omega$		_5 5			30	_	MHz
oub cutoff frequency		C _L = 15 pF	(Note 1)	J	Ŭ		30		IVII IZ
Total harmonic distortion	_	$R_L = 10 \text{ k}\Omega$		_5 5		_	0.03		%
Total Harmonic distortion		f = 1 kHz	(Note 2)	J			0.03		70
-50dB feed through frequency	_	$R_L = 1 \text{ k}\Omega$	(Note 3)	-5	5	_	600	_	kHz
-50dB crosstalk frequency	_	$R_L = 1 k\Omega$	(Note 4)	-5	5	_	1	_	MHz
Crosstalk (control-OUT)	_	$R_{IN} = 1 k\Omega$		0	5	_	200	_	
		$R_{OUT} = 10 \text{ k}\Omega$		0	10	_	400	_	mV
		C _L = 15 pF		0	15	_	600	_	
Input capacitance	C _{IN}	Control input				_	5	7.5	pF
піриї сарасітанс с		Switch I/O					10	_	ρι
Feed through capacitance	C _{IN-OUT}		_			_	0.5	_	pF

Note 1: Sine wave of ± 2.5 p-p shall be used for V_{is} and the frequency of 20 log 10 $\frac{V_{OS}}{V_{is}} = -3 \text{dB}$ shall be f_{max} .

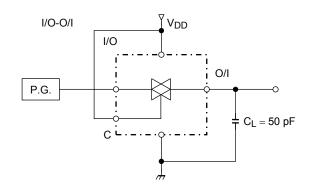
Note 2: V_{is} shall be sine wave of $\pm 2.5 V_{p-p}$

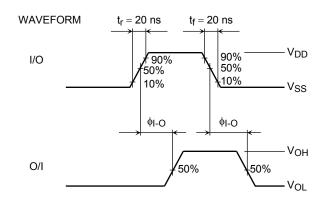
Note 3: Sine wave of $\pm 2.5 \ V_{p-p}$ shall be used for V_{is} and the frequency of 20 log 10 $\frac{V_{OUT}}{V_{is}} = -50 \text{dB}$ shall be feed-through.

Note 4: Sine wave of $\pm 2.5 \ V_{p-p}$ shall be used for V_{is} and the frequency of 20 log 10 $\frac{V_{OUT}}{V_{is}} = -50 \text{dB}$ shall be crosstalk.

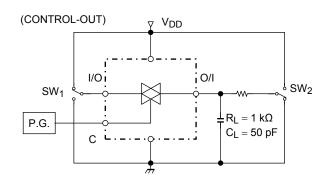
Circuit for Measurement of Electrical Characteristics

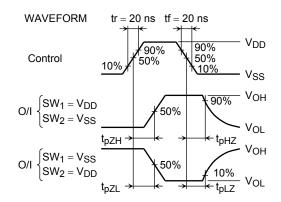
φ_{I-O}



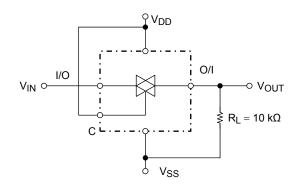


2. t_{pZH}, t_{pHL}, t_{pLZ}, t_{pHZ}



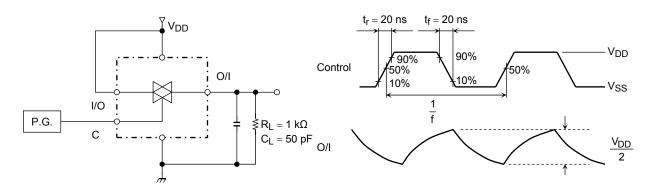


3. RON

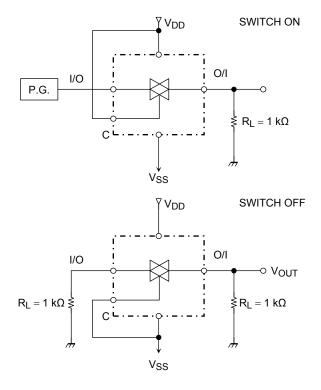


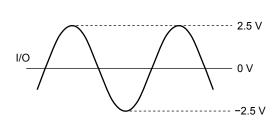
$$R_{ON} = 10 \times \frac{\left(V_{IN} - V_{OUT}\right)}{V_{OUT}} \left[k\Omega\right]$$

4. fmax (C)

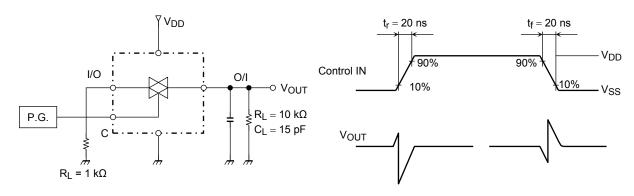


5. Crosstalk between Any Two Switches

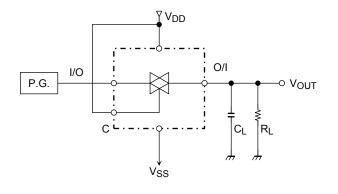


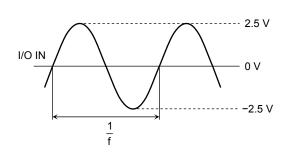


6. Crosstalk, Control to Input



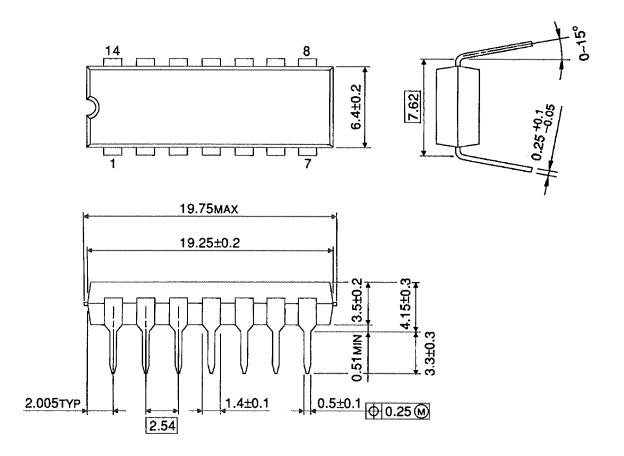
7. Total Harmonic Distortion, fmax (I-O), Feedthrough





Package Dimensions

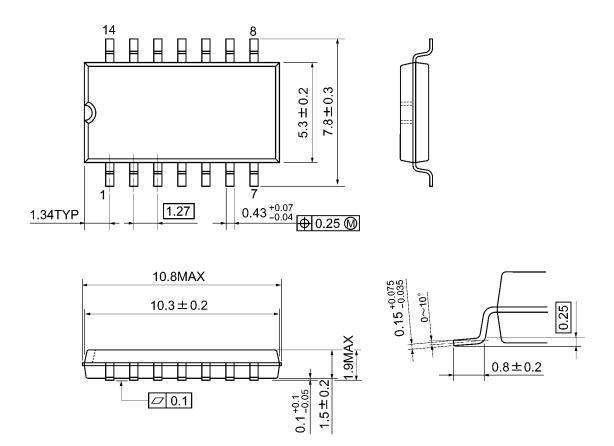
DIP14-P-300-2.54 Unit: mm



Weight: 0.96 g (typ.)

Package Dimensions

SOP14-P-300-1.27A Unit: mm

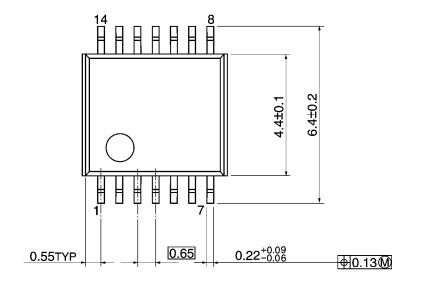


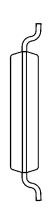
Weight: 0.18 g (typ.)

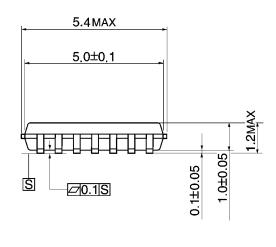
Package Dimensions

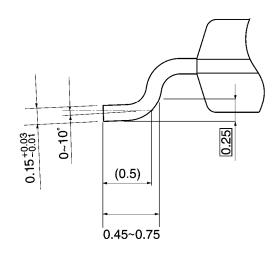
TSSOP14-P-0044-0.65A

Unit: mm









Weight: 0.06 g (typ.)

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