TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74VHCT138AF,TC74VHCT138AFT,TC74VHCT138AFK

3-to-8 Line Decoder

The TC74VHCT138 is an advanced high speed CMOS 3-to-8 LINE DECODER fabricated with silicon gate C^2MOS technology. It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

When the device is enabled, 3 Binary Select inputs (A, B and C) determine which one of the outputs ($\overline{Y}0$ to $\overline{Y}7$) will go low.

When enable input G1 is held low or either $\overline{G}2A$ or $\overline{G}2B$ is held high, decoding function is inhibited and all outputs go high. G1, $\overline{G}2A$, and $\overline{G}2B$ inputs are provided to ease cascade connection and for use as an address decoder for memory systems.

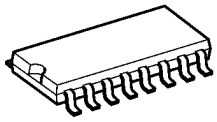
The input voltage are compatible with TTL output voltage.

This device may be used as a level converter for interfacing 3.3 V to 5 V system.

Input protection and output circuit ensure that 0 to 5.5 V can be applied to the input and output (Note) pins without regard to the supply voltage. These structure prevents device destruction due to mismatched supply and input/output voltages such as battery back up, hot board insertion, etc.

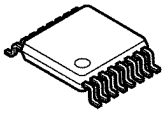
Note: V_{CC} = 0 V

TC74VHCT138AF



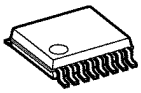
SOP16-P-300-1.27A

TC74VHCT138AFT



TSSOP16-P-0044-0.65A

TC74VHCT138AFK



VSSOP16-P-0030-0.50

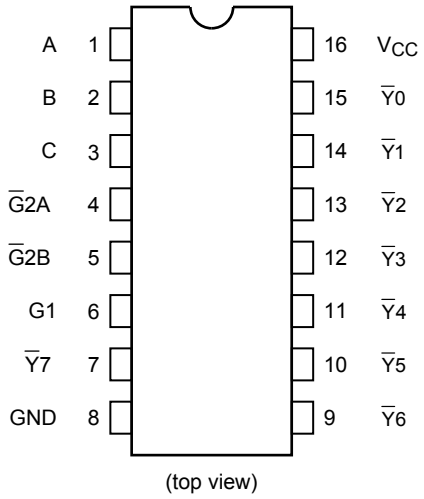
Weight

SOP16-P-300-1.27A:	0.18 g (typ.)
TSSOP16-P-0044-0.65A:	0.06 g (typ.)
VSSOP16-P-0030-0.50:	0.02 g (typ.)

Features

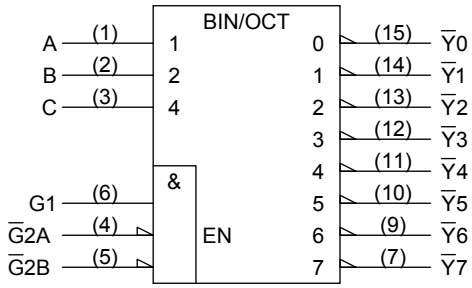
- High speed: t_{pd} = 7.6 ns (typ.) at V_{CC} = 5 V
- Low power dissipation: I_{CC} = 4 μA (max) at Ta = 25°C
- Compatible with TTL inputs: V_{IL} = 0.8 V (max)
  V_{IH} = 2.0 V (min)
- Power down protection is provided on all inputs and outputs
- Balanced propagation delays: $t_{pLH} \approx t_{pHL}$
- Pin and function compatible with the 74 series (74AC/HC/F/ALS/LS etc.) 138 type.

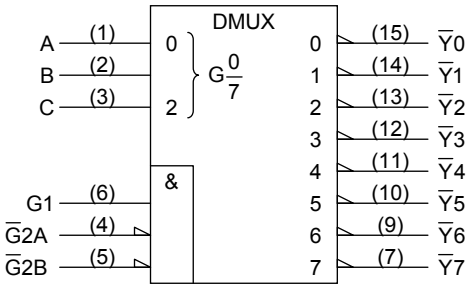
Pin Assignment



(top view)

IEC Logic Symbol



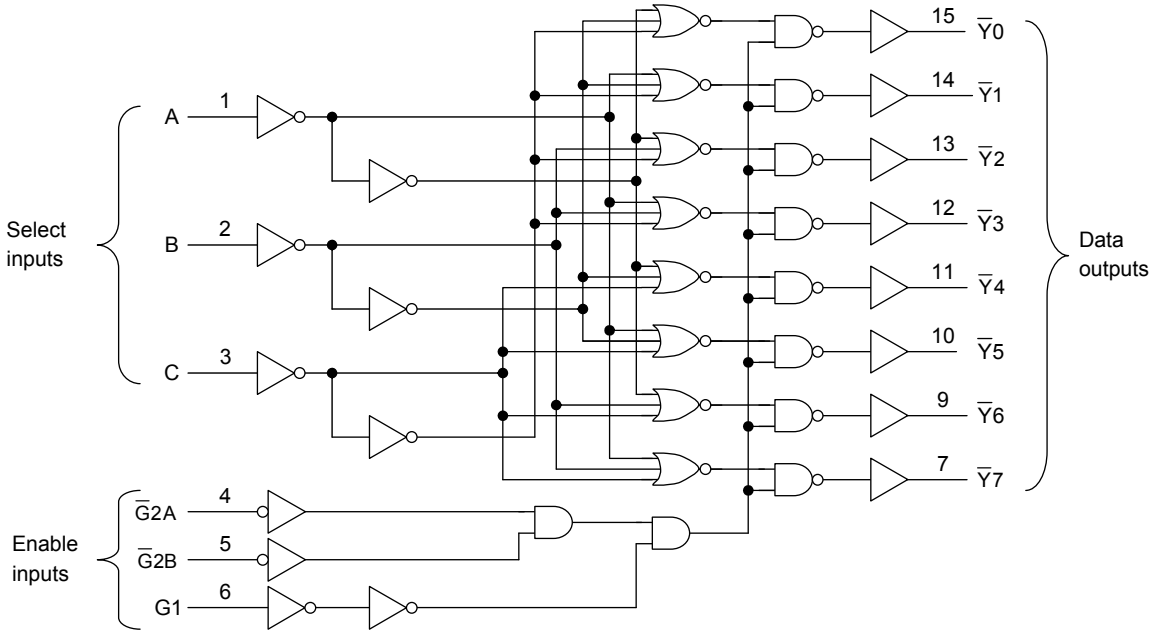


Truth Table

Inputs						Outputs								Selected Output
Enable			Select			$\overline{Y}$0	$\overline{Y}$1	$\overline{Y}$2	$\overline{Y}$3	$\overline{Y}$4	$\overline{Y}$5	$\overline{Y}$6	$\overline{Y}$7	
G1	$\overline{G}$2A	$\overline{G}$2B	C	B	A									
L	X	X	X	X	X	H	H	H	H	H	H	H	H	None
X	H	X	X	X	X	H	H	H	H	H	H	H	H	None
X	X	H	X	X	X	H	H	H	H	H	H	H	H	None
H	L	L	L	L	L	L	H	H	H	H	H	H	H	$\overline{Y}$0
H	L	L	L	L	H	H	L	H	H	H	H	H	H	$\overline{Y}$1
H	L	L	L	H	L	H	H	L	H	H	H	H	H	$\overline{Y}$2
H	L	L	L	H	H	H	H	H	L	H	H	H	H	$\overline{Y}$3
H	L	L	H	L	L	H	H	H	H	L	H	H	H	$\overline{Y}$4
H	L	L	H	L	H	H	H	H	H	H	L	H	H	$\overline{Y}$5
H	L	L	H	H	L	H	H	H	H	H	H	L	H	$\overline{Y}$6
H	L	L	H	H	H	H	H	H	H	H	H	H	L	$\overline{Y}$7

X: Don’t care

System Diagram



Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating		Unit
Supply voltage range	V_{CC}	−0.5 to 7.0		V
DC input voltage	V_{IN}	−0.5 to 7.0		V
DC output voltage	V_{OUT}	−0.5 to 7.0	(Note 2)	V
		−0.5 to V_{CC} + 0.5	(Note 3)	
Input diode current	I_{IK}	−20		mA
Output diode current	I_{OK}	±20	(Note 4)	mA
DC output current	I_{OUT}	±25		mA
DC V_{CC}/ground current	I_{CC}	±75		mA
Power dissipation	P_D	180		mW
Storage temperature	T_{stg}	−65 to 150		°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.
Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: V_{CC} = 0 V

Note 3: High or low state. I_{OUT} absolute maximum rating must be observed.

Note 4: $V_{OUT} < GND$, $V_{OUT} > V_{CC}$

Operating Ranges (Note 1)

Characteristics	Symbol	Rating		Unit
Supply voltage	V_{CC}	4.5 to 5.5		V
Input voltage	V_{IN}	0 to 5.5		V
Output voltage	V_{OUT}	0 to 5.5	(Note 2)	V
		0 to V_{CC}	(Note 3)	
Operating temperature	T_{opr}	−40 to 85		°C
Input rise and fall time	dt/dV	0 to 20		ns/V

Note 1: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

Note 2: V_{CC} = 0 V

Note 3: High or low state

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = −40 to 85°C		Unit
				V_{CC} (V)	Min	Typ.	Max	Min	Max	
High-level input voltage	V_{IH}	—		4.5 to 5.5	2.0	—	—	2.0	—	V
Low-level input voltage	V_{IL}	—		4.5 to 5.5	—	—	0.8	—	0.8	V
High-level output voltage	V_{OH}	V_{IN} = V_{IH} or V_{IL}	I_{OH} = −50 μA	4.5	4.40	4.50	—	4.40	—	V
			I_{OH} = −8 mA	4.5	3.94	—	—	3.80	—	
Low-level output voltage	V_{OL}	V_{IN} = V_{IH} or V_{IL}	I_{OL} = 50 μA	4.5	—	0.0	0.1	—	0.1	V
			I_{OL} = 8 mA	4.5	—	—	0.36	—	0.44	
Input leakage current	I_{IN}	V_{IN} = 5.5 V or GND		0 to 5.5	—	—	±0.1	—	±1.0	μA
Quiescent supply current	I_{CC}	V_{IN} = V_{CC} or GND		5.5	—	—	4.0	—	40.0	μA
	I_{CCT}	Per input: V_{IN} = 3.4 V Other input: V_{CC} or GND		5.5	—	—	1.35	—	1.50	mA
Output leakage current	I_{OPD}	V_{OUT} = 5.5 V		0	—	—	0.5	—	5.0	μA

AC Characteristics (input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = −40 to 85°C		Unit
			V_{CC} (V)	C_L (pF)	Min	Typ.	Max	Min	Max	
Propagation delay time (A, B, C- $\overline{Y}$)	t_{pLH} t_{pHL}	—	5.0 ± 0.5	15	—	7.6	10.4	1.0	12.0	ns
				50	—	8.1	11.4	1.0	13.0	
Propagation delay time (G1- $\overline{Y}$)	t_{pLH} t_{pHL}	—	5.0 ± 0.5	15	—	6.6	9.1	1.0	10.5	ns
				50	—	7.1	10.1	1.0	11.5	
Propagation delay time ($\overline{G2}$ - $\overline{Y}$)	t_{pLH} t_{pHL}	—	5.0 ± 0.5	15	—	7.0	9.6	1.0	11.0	ns
				50	—	7.5	10.6	1.0	12.0	
Input capacitance	C_{IN}	—			—	4	10	—	10	pF
Power dissipation capacitance	C_{PD}	(Note)			—	49	—	—	—	pF

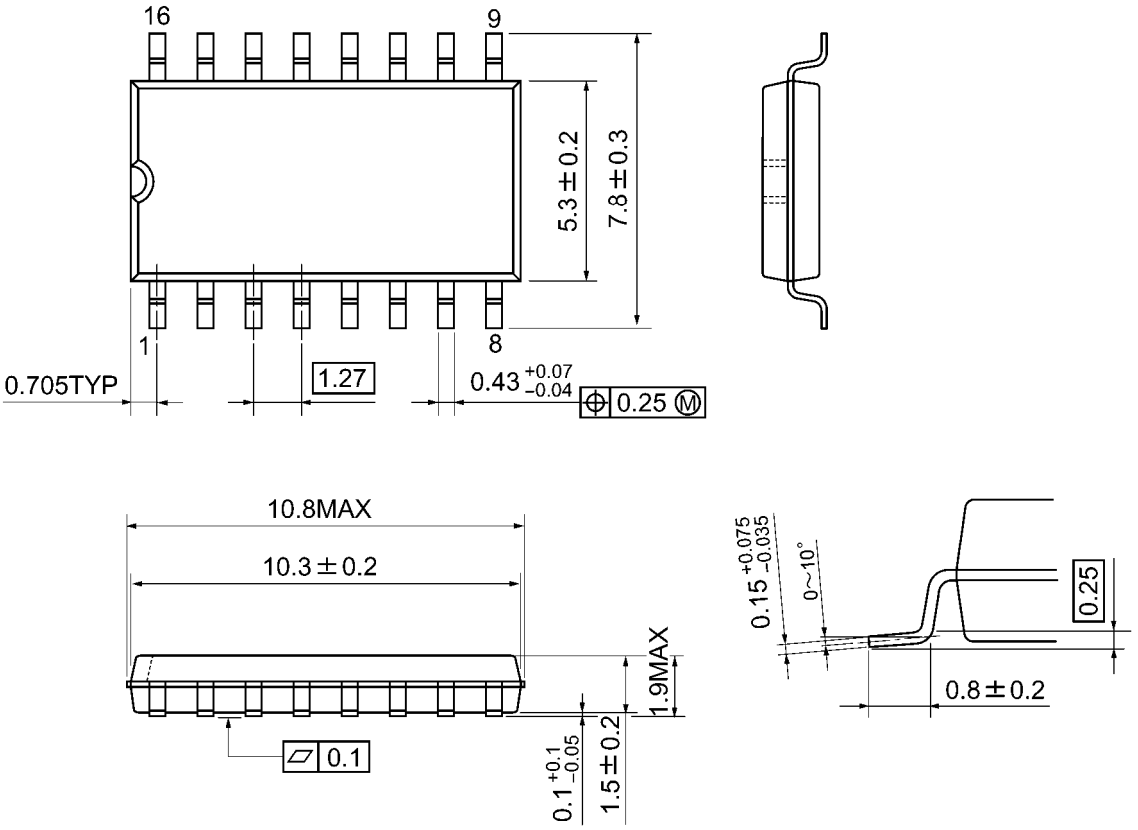
Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

$$I_{CC\ (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

Package Dimensions

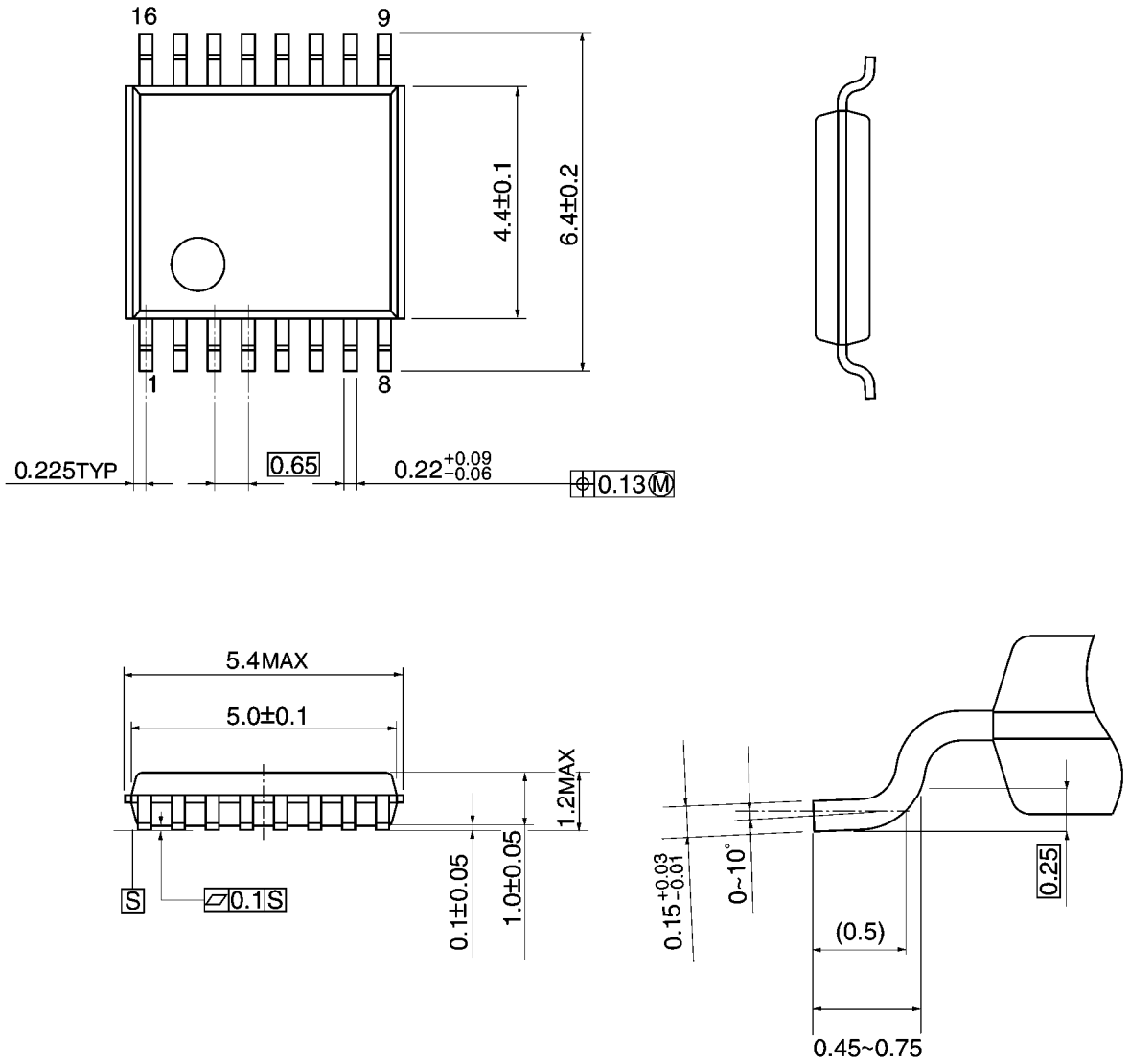
SOP16-P-300-1.27A Unit: mm



Weight: 0.18 g (typ.)

Package Dimensions

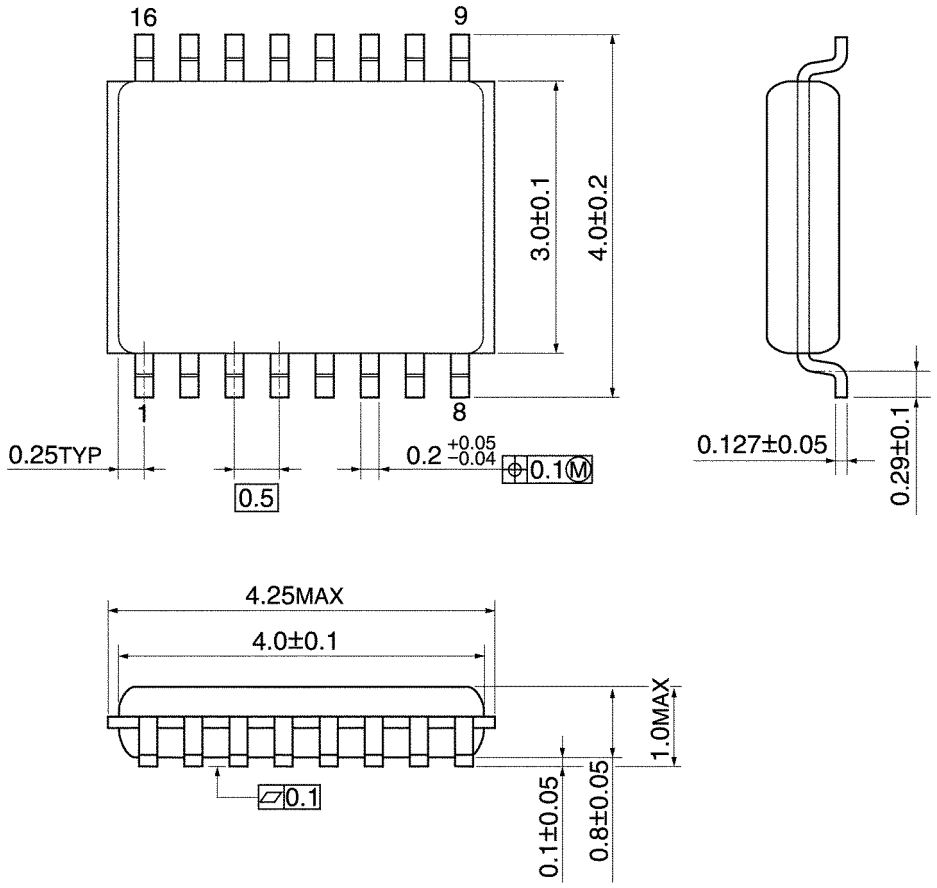
TSSOP16-P-0044-0.65A Unit: mm



Weight: 0.06 g (typ.)

Package Dimensions

VSSOP16-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)

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