

Photocouplers Photorelay

TLP3306

1. Applications

- · High-Speed Memory Testers
- · High-Speed Logic IC Testers
- · Medical instruments
- · Power supplies

2. General

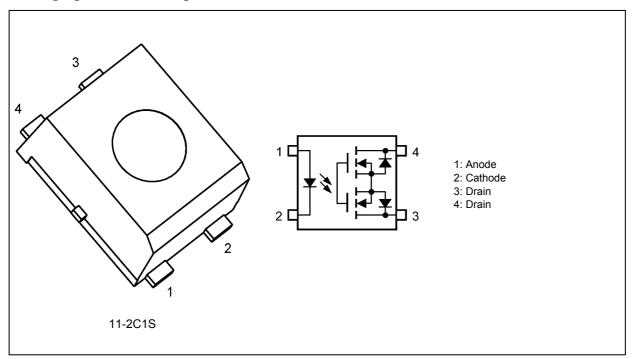
The TLP3306 is a photorelay in a 4-pin USOP that consists of a photo MOSFET optically coupled with an infrared light emitting diode. Even though the TLP3306 photorelay is housed in a tiny USOP package, it offers low on-resistance and high isolation voltage. The ability to switch high-current loads reliably makes it suitable for space-limited switching applications.

3. Features

- (1) Normally off (1-Form-A)
- (2) OFF-state output terminal voltage: 75 V (min)
- (3) Trigger LED current: 3 mA (max)
- (4) ON-state current: 400 mA (max)
- (5) ON-state resistance: 1.0Ω (typ.), 1.5Ω (max)
- (6) OFF-state Capacitance: 30 pF (typ.)
- (7) Isolation voltage: 500 Vrms (min)
- (8) Safety standards

UL under-application: UL1577 File No.E67349

4. Packaging and Pin Configuration





5. Internal Circuit

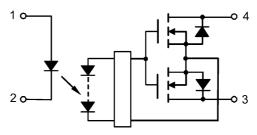


Fig. 5.1 Internal Circuit

6. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25°C)

	Characteristics	Symbol	Note	Rating	Unit	
LED	Input forward current		l _F		50	mA
	Input forward current derating (T _a ≥ 25°C)	$\Delta I_F / \Delta T_a$		-0.5	mA/°C
	Input reverse voltage		V_R		5	V
	Input power dissipation		P_{D}		50	mW
	Junction temperature		Tj		125	°C
Detector	OFF-state output terminal voltage		V_{OFF}		75	V
	ON-state current		I _{ON}		400	mA
	ON-state current derating (T _a ≥ 25°C)	$\Delta I_{ON}/\Delta T_a$		-4.0	mA/°C
	ON-state current (pulsed) (t = 100	ms, Duty = 1/10)	I _{ONP}		1.2	Α
	Output power dissipation		Po		200	mW
	Junction temperature		Tj		125	°C
Common	Storage temperature		T _{stg}		-40 to 125	
	Operating temperature		T_{opr}		-40 to 85	
	Lead soldering temperature	(10 s)	T _{sol}		260	
	Isolation voltage AC, 1	min, R.H. ≤ 60%	BV _S	(Note 1)	500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: This device is considered as a two-terminal device: Pins 1 and 2 are shorted together, and pins 3 and 4 are shorted together.

Caution: This device is sensitive to electrostatic discharge (ESD). Extreme ESD conditions should be guarded against by using proper antistatic precautions for the worktable, operator, solder iron, soldering equipment and so on.

7. Recommended Operating Conditions (Note)

Characteristics	Symbol	Note	Min	Тур.	Max	Unit
Input forward current	I _F		5	7.5	20	mA
Operating temperature	T _{opr}		-20	_	65	°C

Note: The recommended operating conditions are given as a design guide necessary to obtain the intended performance of the device. Each parameter is an independent value. When creating a system design using this device, the electrical characteristics specified in this datasheet should also be considered.



8. Electrical Characteristics (Unless otherwise specified, T_a = 25°C)

	Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
LED	Input forward voltage	V _F		I _F = 10 mA	1.0	1.15	1.3	V
	Input reverse current	I _R		V _R = 5 V	1		10	μΑ
	Input capacitance	Ct		V = 0 V, f = 1 MHz	1	15	_	pF
Detector	OFF-state current	I _{OFF}		V _{OFF} = 75 V	_	_	1	nA
	Output capacitance	C _{OFF}		V = 0 V, f = 100 MHz, t < 1s	_	30	_	pF

9. Coupled Electrical Characteristics (Unless otherwise specified, T_a = 25°C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	I _{FT}		I _{ON} = 100 mA	_	0.5	3	mA
Return LED current	I _{FC}		I _{OFF} = 10 μA	0.1			mA
ON-state resistance	R _{ON}		I _{ON} = 400 mA, I _F = 5 mA, t < 1 s	_	1.0	1.5	Ω

10. Isolation Characteristics (Unless otherwise specified, Ta = 25°C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Total capacitance (input to output)	C _S	(Note 1)	V _S = 0 V, f = 1 MHz	_	0.4	_	pF
Isolation resistance	R _S	(Note 1)	V_S = 500 V, R.H. \leq 60%	5 × 10 ¹⁰	1014		Ω
Isolation voltage	BVS	(Note 1)	AC, 1 min	500			Vrms
			AC, 1s in oil	_	1000	_	
			DC, 1 min, in oil		1000	_	Vdc

Note 1: This device is considered as a two-terminal device: Pins 1 and 2 are shorted together, and pins 3 and 4 are shorted together.

11. Switching Characteristics (Unless otherwise specified, $T_a = 25^{\circ}C$)

Characteristics	Symbol	Note	Test Condition	Min	Тур	Max	Unit
Turn-on time	t _{ON}		See Fig. 11.1	_	0.4	2	ms
Turn-off time	t _{OFF}		$R_L = 200 \Omega$, $V_{DD} = 20 V$, $I_F = 5 mA$		0.2	1	

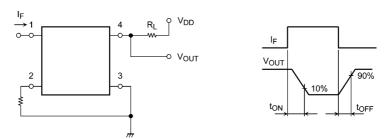


Fig. 11.1 Switching Time Test Circuit

12. Characteristics Curves

12.1. Characteristics Curves (Note)

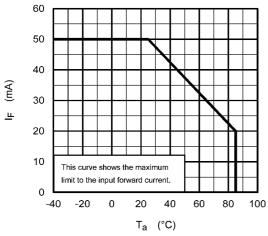


Fig. 12.1.1 I_F - T_a

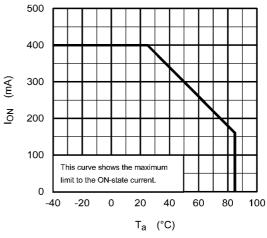


Fig. 12.1.2 I_{ON} - T_a

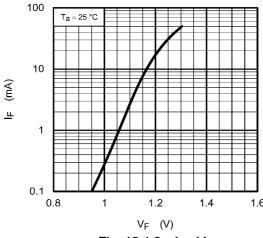


Fig. 12.1.3 I_F - V_F

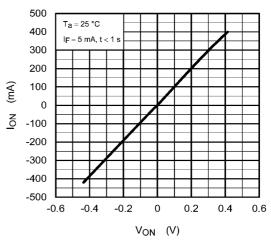


Fig. 12.1.4 I_{ON} - V_{ON}

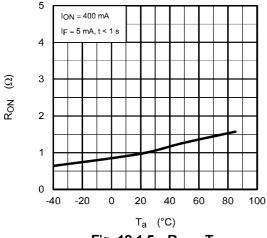


Fig. 12.1.5 R_{ON} - T_a

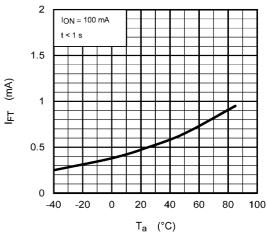


Fig. 12.1.6 I_{FT} - T_a

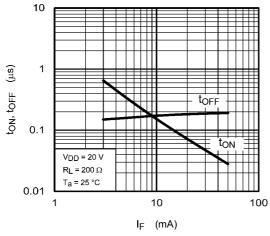


Fig. 12.1.7 t_{ON}, t_{OFF} - I_F

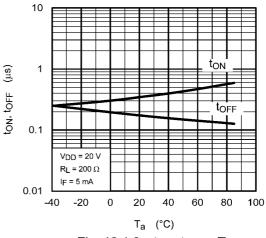


Fig. 12.1.8 t_{ON}, t_{OFF} - T_a

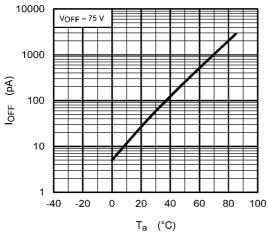


Fig. 12.1.9 I_{OFF} - T_a

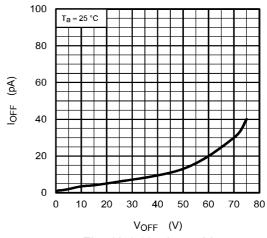


Fig. 12.1.10 I_{OFF} - V_{OFF}

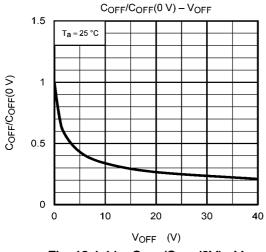


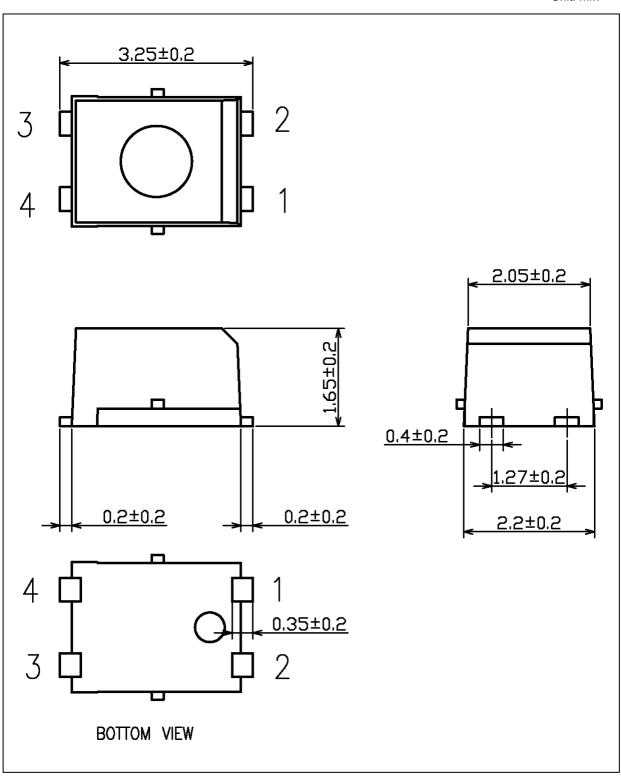
Fig. 12.1.11 C_{OFF}/C_{OFF}(0V) - V_{OFF}

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Package Dimensions

Unit: mm



Weight: 0.03 g (typ.)

	Package Name(s)
TOSHIBA: 11-2C1S	



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