TOSHIBA Photocoupler Photorelay

TLP174GA

Modem·Fax Cards, Modems in PC Telecommunications
PBX

Measurement Equipment

The Toshiba TLP174GA consists of an aluminum gallium arsenide infrared emitting diode optically coupled to a photo-MOSFET in a SOP, which is suitable for surface mount assembly.

The TLP174GA is suitable for the modem applications which require space savings. $\,$

• 4-pin SOP (2.54SOP4): Height = 2.1 mm, Pitch = 2.54 mm

• 1-Form-A

• Peak Off-state voltage: 400 V (min)

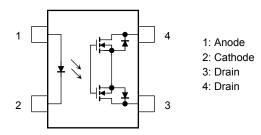
• Trigger LED current: 3 mA (max)

• On-state current: 120 mA (max)

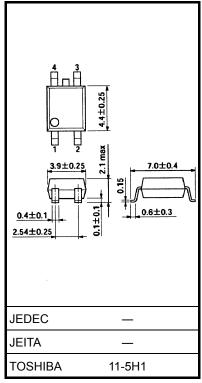
• Limit current: $150 \text{ mA} \sim 300 \text{ mA} \text{ (t = 5 ms)}$

On-state resistance: 35 Ω (max)
 Isolation voltage: 1500 Vrms (min)

Pin Configuration (top view)



Unit: mm



Weight: 0.1 g (typ.)

Absolute Maximum Ratings (Ta = 25℃)

Characteristics		Symbol	Rating	Unit
	Forward current	lF	50	mA
LED	Forward current derating $(Ta \ge 25^{\circ}C)$	ΔI _F /°C	-0.5	mA/°C
	Peak forward current (100 μs pulse, 100 pps)	I _{FP}	1	А
	Reverse voltage	V_{R}	5	V
	Junction temperature	Tj	125	°C
	Off-state output terminal voltage	V _{OFF}	400	V
	On-state current	I _{ON}	120	mA
Detector	On-state current derating (Ta ≥ 25°C)	Δl _{ON} /°C	-1.2	mA/°C
	Junction temperature	Tj	125	°C
Storage temperature range		T _{stg}	-55~125	°C
Operating temperature range		T _{opr}	-40~85	°C
Lead soldering temperature (10 s)		T _{sol}	260	°C
Isolation voltage (AC, 1 min, R.H. \leq 60%) (Note 1)		BVS	1500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device considered a two-terminal device: LED side pins shorted together, and detector side pins shorted together.

Recommended Operating Conditions

Characteristics	Symbol	Min	Тур.	Max	Unit
Supply voltage	V_{DD}			320	V
Forward current	l _F	5	7.5	25	mA
On-state current	I _{ON}			120	mA
Operating temperature	T _{opr}	-20		65	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Electrical Characteristics (Ta = 25℃)

Characteristics		Symbol	ol Test Condition		Тур.	Max	Unit
	Forward voltage	V _F	I _F = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	I _R	V _R = 5 V	_	_	10	μΑ
	Capacitance	C _T	V = 0, f = 1 MHz	_	30	_	pF
Detector	Off-state current	l _{OFF}	V _{OFF} = 400 V	_	_	1	μΑ
	Capacitance	C _{OFF}	V = 0, f = 1 MHz	_	70	_	pF

Coupled Electrical Characteristics (Ta = 25℃)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	I _{FT}	I _{ON} = 120 mA	_	1	3	mA
Close LED current	I _{FC}	I _{OFF} = 100 μA	0.1	_	_	mA
Load current limiting	I _{LIM}	$I_{ON} = 5 \text{ mA}, V_{DD} = 5 \text{ V}, t < 5 \text{ ms}$	150		300	Ω
On-state resistance	R _{ON}	I _{ON} = 120 mA, I _F = 5 mA	_	17	35	22

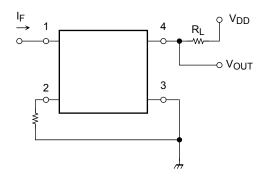
Isolation Characteristics (Ta = 25℃)

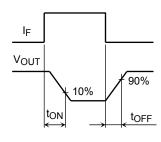
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance input to output	Cs	V _S = 0 V, f = 1 MHz	_	0.8	_	pF
Isolation resistance	R _S	V _S = 500 V, R.H. ≦ 60%	5×10^{10}	10 ¹⁴	_	Ω
Isolation voltage		AC, 1 min	1500	_	_	Vrms
	BV_S	AC, 1 s, in oil 3000	3000	_	VIIIIS	
		DC, 1 min, in oil	_	3000	_	Vdc

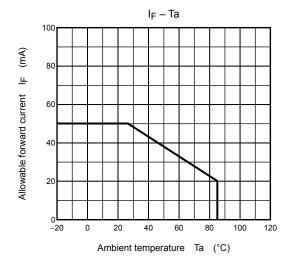
Switching Characteristics (Ta = 25°C)

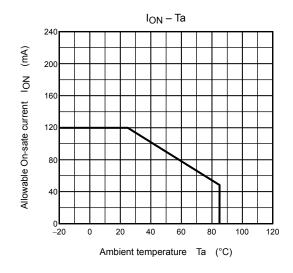
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Turn-on time	t _{ON}	$R_L = 200 \Omega$	_	0.3	1	ms
Turn-off time	toff	$V_{DD} = 20 \text{ V, I}_{F} = 5 \text{ mA}$ (Note 2)	_	0.1	1	1113

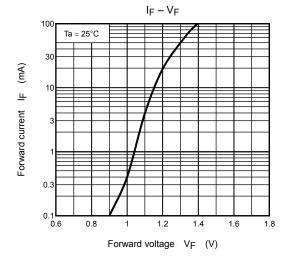
Note 2: Switching time test circuit

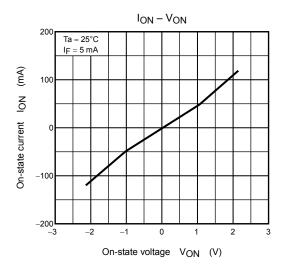


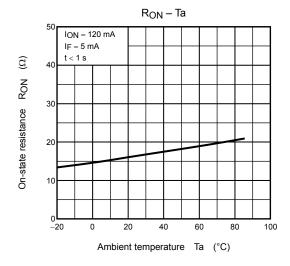


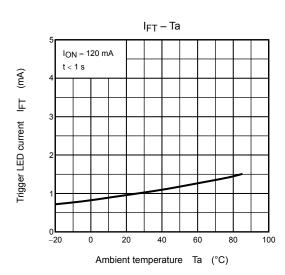




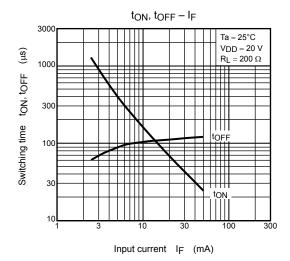


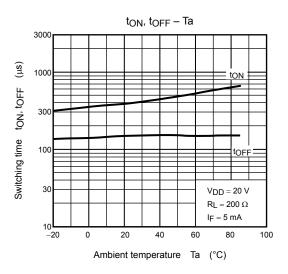


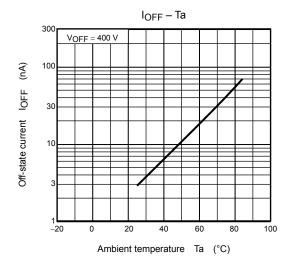




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