#### TOSHIBA PHOTOCOUPLER GaAs IRED & PHOTO-TRANSISTOR

## **TLP291**

# Power Supplies Programmable Controllers Hybrid ICs

TLP291 consists of photo transistor, optically coupled to a gallium arsenide infrared emitting diode. TLP291 is housed in the SO4 package, very small and thin coupler.

Since TLP291 is guaranteed wide operating temperature (Ta=-55 to 110 °C) and high isolation voltage (3750Vrms), it's suitable for high-density surface mounting applications such as small switching power supplies and programmable controllers.

Collector-Emitter Voltage : 80 V (min)
 Current Transfer Ratio : 50% (min)
 Rank GB : 100% (min)
 Isolation Voltage : 3750 Vrms (min)

Operation temperature: -55 to 110 °C

UL recognized : UL1577, File No. E67349

• cUL approved : CSA Component Acceptance Service No.5A,

File No. 67349

• SEMKO aprroved: EN 60065: 2002, Approved no. 1200315

EN 60950-1: 2001, EN 60335-1: 2002,

Approved no. 1200315

BSI approved : BS EN 60065: 2002, Approved no. 9036

: BS EN 60950-1: 2006, Approved no. 9037

Option (V4)

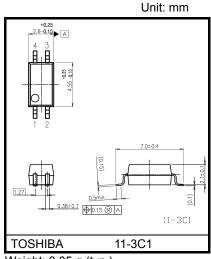
VDE approved: EN 60747-5-5 Certificate, No. 40009347 Maximum operating insulation voltage: 707 Vpk Highest permissible over-voltage: 6000 Vpk

(Note) When a EN 60747-5-5 approved type is needed, please designate the "Option(V4)"

Construction Mechanical Rating Creepage distance:5.0mm(min)

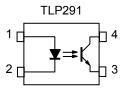
Clearance:5.0mm(min)

Insultion thickness:0.4mm(min)



Weight: 0.05 g (typ.)

## **Pin Configuration**



1:ANODE 2:CATHODE 3:EMITTER 4:COLLECTOR

## Current Transfer Ratio (CTR) Rank (Unless otherwise specified, Ta = 25°C)

	Classification		sfer Ratio (%) / I <sub>F</sub> )		
TYPE	(Note1)	I <sub>F</sub> = 5 mA, V <sub>CE</sub> = 5 V, Ta = 25°C		Marking of Classification	
		Min	Max		
	Blank	50	400	Blank, YE, Y+, GR, GB, G, G+,B	
	Rank Y	50	150	YE	
	Rank GR	100	300	GR	
TLP291	Rank GB	100	400	GB	
12.201	Rank YH	75	150	Y+	
	Rank GRL	100	200	G	
	Rank GRH	150	300	G+	
	Rank BLL	200	400	В	

Note1: Specify both the part number and a rank in this format when ordering

(e.g.) rank GB: TLP291 (GB,E

For safety standard certification, however, specify the part number alone.

(e.g.)TLP291 (GB,E: TLP291

## Absolute Maximum Ratings (Note)( Unless otherwise specified, Ta = 25°C)

	CHARACTERISTIC	SYMBOL	NOTE	RATING	UNIT
	Input forward current	IF		50	mA
	Input forward current derating (Ta≥90°C)	ΔI <sub>F</sub> / <b>Δ</b> Ta		-1.5	mA /°C
	Input forward current (pulsed )	I <sub>FP</sub>	(Note 2)	1	Α
LED	Input reverse voltage	V <sub>R</sub>		5	V
	Input power dissipation	P <sub>D</sub>		100	mW
	Input power dissipation derating (Ta ≥ 90°C)	ΔΡ <sub>D</sub> /ΔΤα		-3.0	mW/°C
	Junction temperature	Tj		125	°C
	Collector-emitter voltage	V <sub>CEO</sub>		80	V
~	Emitter-collector voltage	V <sub>ECO</sub>		7	V
DETECTOR	Collector current	IC		50	mA
ETE(	Collector power dissipation	PC		150	mW
□	Collector power dissipation derating(Ta≥25°C)	ΔP <sub>C</sub> /ΔTa		-1.5	mW /°C
	Junction temperature	Tj		125	°C
Оре	erating temperature range	T <sub>opr</sub>		-55 to 110	°C
Storage temperature range		T <sub>stg</sub>		-55 to 125	°C
Lead soldering temperature		T <sub>sol</sub>		260 (10s)	°C
Total package power dissipation		P <sub>T</sub>		200	mW
Tota	al package power dissipation derating(Ta≥25°C)	ΔP <sub>T</sub> /ΔTa		-2.0	mW /°C
Isola	ation voltage	BVS	(Note3)	3750	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note2: Pulse width  $\leq 100 \mu s$ , frequency 100Hz

Note3: AC, 1 minute, R.H.≤60%, Device considered a two terminal device: LED side pins shorted together and DETECTOR side pins shorted together.

### Electrical Characteristics (Unless otherwise specified, Ta = 25°C)

	CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
	Input forward voltage V <sub>F</sub>		I <sub>F</sub> = 10 mA	1.1	1.25	1.4	V
E	$\square$ Input reverse current I <sub>R</sub> V <sub>R</sub> = 5 V		V <sub>R</sub> = 5 V	1	-	5	μА
	Input capacitance	C <sub>T</sub>	V = 0 V, f = 1 MHz	-	30	-	pF
	Collector-emitter breakdown voltage V <sub>(BR) CEO</sub>		I <sub>C</sub> = 0.5 mA	80	-	-	V
O.R	Emitter-collector breakdown voltage	V <sub>(BR) ECO</sub>	I <sub>E</sub> = 0.1 mA	7	-	-	V
DETECTOR	Dark current	1	V <sub>CE</sub> = 48 V	1	0.01	0.08	μΑ
DEJ	Dark current	ICEO	V <sub>CE</sub> = 48 V, Ta = 85°C	1	2	50	μΑ
	Collector-emitter capacitance C <sub>CE</sub>		V = 0 V, f = 1 MHz	1	10	1	pF

## Coupled Electrical Characteristics (Unless otherwise specified, Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Current transfer ratio	Ic / I <sub>F</sub>	I <sub>F</sub> = 5 mA, V <sub>CE</sub> = 5 V	50	-	400	%
Current transfer fatto	IC7 IF	Rank GB	100	-	400	/0
Saturated current transfer ratio	lo / le /	I <sub>F</sub> = 1 mA, V <sub>CE</sub> = 0.4 V	-	60	-	%
Saturated current transfer ratio	I <sub>C</sub> / I <sub>F (sat)</sub>	Rank GB	30	-	-	/0
		I <sub>C</sub> = 2.4 mA, I <sub>F</sub> = 8 mA	-	-	0.3	
Collector-emitter saturation voltage	V <sub>CE</sub> (sat)	I <sub>C</sub> = 0.2 mA, I <sub>F</sub> = 1 mA	-	0.2	-	V
		Rank GB		-	0.3	
OFF-state collector current	I <sub>C (off)</sub>	V <sub>F</sub> = 0.7 V, V <sub>CE</sub> = 48 V		-	10	μА

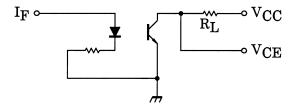
## Isolation Characteristics (Unless otherwise specified, Ta = 25°C)

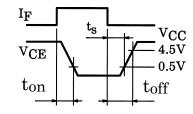
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Total capacitance (input to output)	CS	V <sub>S</sub> = 0 V, f = 1 MHz	-	0.8	-	pF
Isolation resistance	R <sub>S</sub>	V <sub>S</sub> = 500 V, R.H.≤60%		10 <sup>14</sup>	-	Ω
		AC , 1 minute	3750	-	-	Vrms
Isolation voltage	$BV_S$	AC , 1 second, in OIL	-	10000	-	VIIIIS
		DC , 1 minute, in OIL	-	10000	-	Vdc

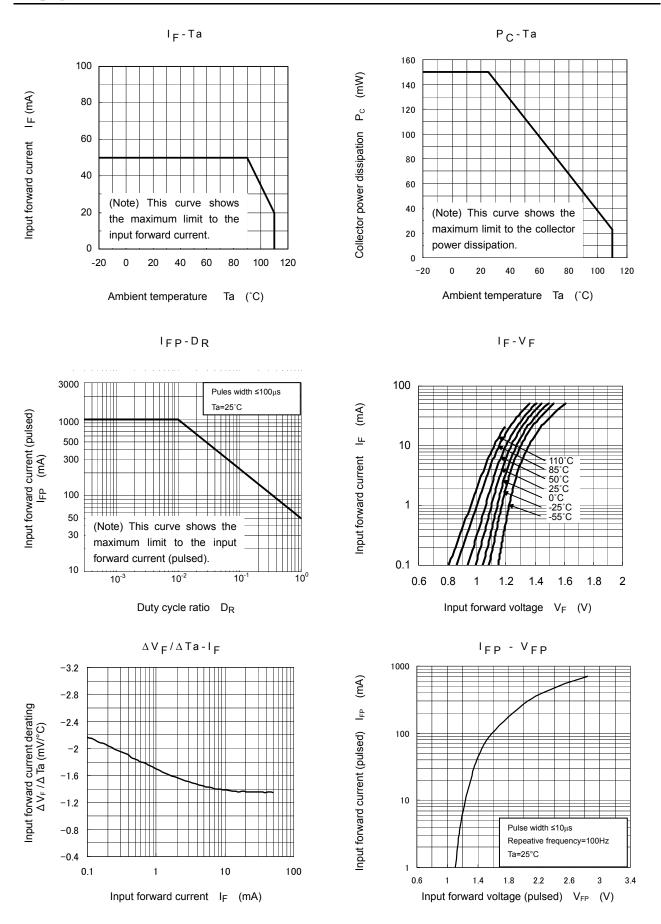
## Switching Characteristics (Unless otherwise specified, Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Rise time	t <sub>r</sub>		-	4	-	
Fall time	t <sub>f</sub>	$V_{CC} = 10 \text{ V}, I_{C} = 2 \text{ mA}$	-	7	-	116
Turn-on time	t <sub>on</sub>	$R_L = 100\Omega$	1	7	1	μS
Turn-off time	t <sub>off</sub>		-	7	-	
Turn-on time	t <sub>on</sub>	$R_L$ = 1.9 kΩ (Fig.1) V <sub>CC</sub> = 5 V, I <sub>F</sub> = 16 mA	1	2	1	
Storage time	t <sub>s</sub>		-	30	-	μS
Turn-off time	t <sub>off</sub>		-	60	-	

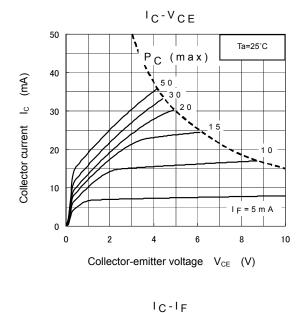
(Fig.1) Switching Time Test Circuit

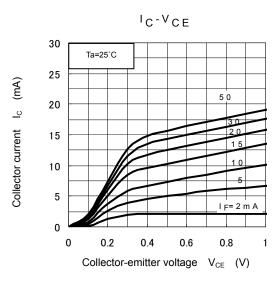


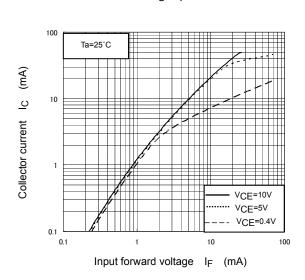


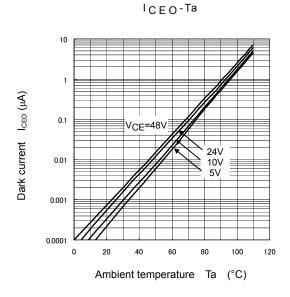


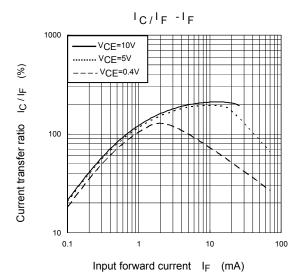
Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



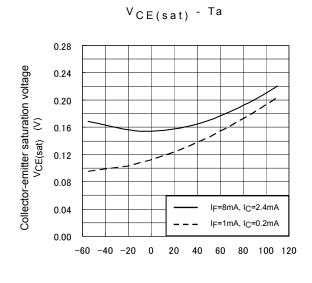




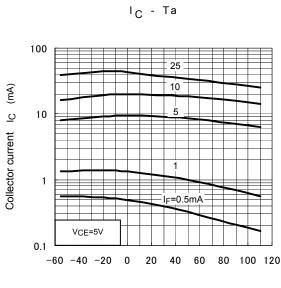




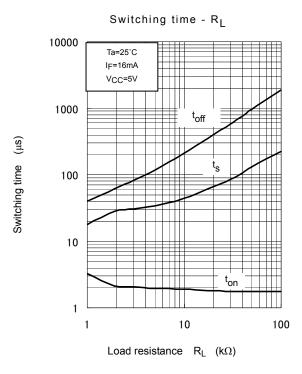
Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Ambient temperature Ta (°C)



Ambient temperature Ta (°C)



Switching time - Ta 1000 toff 100 Switching time (µs) 10 IF=16mA  $R_L=1.9k\Omega$ 0.1 -60 -40 20 40 60 80 100 120 Ambient temperature Ta (°C)

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

## **Soldering and Storage**

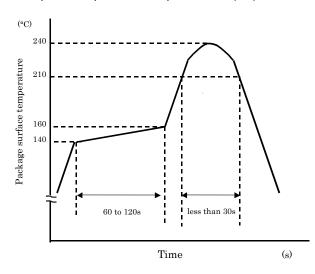
## 1. Soldering

#### 1.1 Soldering

When using a soldering iron or medium infrared ray/hot air reflow, avoid a rise in device temperature as much as possible by observing the following conditions.

### 1) Using solder reflow

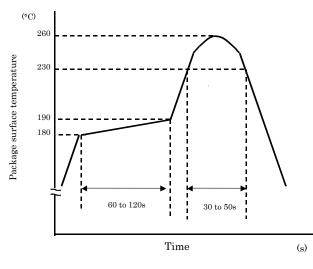
·Temperature profile example of lead (Pb) solder



This profile is based on the device's maximum heat resistance guaranteed value.

Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

·Temperature profile example of using lead (Pb)-free solder



This profile is based on the device's maximum heat resistance guaranteed value.

Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

Reflow soldering must be performed once or twice.

The mounting should be completed with the interval from the first to the last mountings being 2 weeks.

- 2) Using solder flow (for lead (Pb) solder, or lead (Pb)-free solder)
  - · Please preheat it at 150°C between 60 and 120 seconds.
  - · Complete soldering within 10 seconds below 260°C. Each pin may be heated at most once.
- 3) Using a soldering iron

Complete soldering within 10 seconds below 260°C, or within 3 seconds at 350°C. Each pin may be heated at most once.

### 2. Storage

- 1) Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- 2) Follow the precautions printed on the packing label of the device for transportation and storage.
- 3) Keep the storage location temperature and humidity within a range of 5°C to 35°C and 45% to 75%, respectively.
- 4) Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- 5) Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- 6) When restoring devices after removal from their packing, use anti-static containers.
- 7) Do not allow loads to be applied directly to devices while they are in storage.
- 8) If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

## EN 60747-5-5 Option:(V4)

Types : TLP291

Type designations for "option: (V4)", which are tested under EN 60747 requirements.

(e.g.): TLP291 (V4GB-TP,E V4 : EN 60747 option

GB: CTR rank type

TP: Standard tape & reel type

E: [[G]]/RoHS COMPATIBLE (Note4)

Note: Use TOSHIBA standard type number for safety standard application.

(e.g.): TLP291(V4GB-TP,E → TLP291

Note4: Please contact your Toshiba sales representative for details on environmental information such as the product's RoHS compatibility.

RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronics equipment.

### **EN 60747 Isolation Characteristics**

Description	Symbol	Rating	Unit
Application classification  for rated mains voltage ≤ 150Vrms for rated mains voltage ≤ 300Vrms		I-IV I-III	-
Climatic classification		55 / 110 / 21	-
Pollution degree		2	-
Maximum operating insulation voltage	VIORM	707	Vpk
Input to output test voltage, Method A  Vpr=1.5 × V <sub>IORM</sub> , type and sample test tp=10s, partial discharge<5pC	V <sub>pr</sub>	1060	Vpk
Input to output test voltage, Method B  Vpr=1.875 × V <sub>IORM</sub> , 100% production test tp=1s, partial discharge<5pC	V <sub>pr</sub>	1325	Vpk
Highest permissible overvoltage (transient overvoltage, tpr=60s)	V <sub>TR</sub>	6000	Vpk
Safety limiting values (max. permissible ratings in case of fault, also refer to thermal derating curve) current (input current: I <sub>F</sub> , Psi=0mW) power (output or total power dissipation) temperature Insulation resistance	I <sub>si</sub> P <sub>si</sub> T <sub>si</sub>	250 400 150	mA mW °C
V <sub>IO</sub> =500V, Ta=T <sub>Si</sub>	R <sub>si</sub>	≥ 10 <sup>9</sup>	Ω

## **Insulation Related Specifications**

Minimum creepage distance	Cr	5.0mm
Minimum clearance	CI	5.0mm
Minimum insulation thickness	ti	0.4mm
Comparative tracking index	CTI	175

 If a printed circuit is incorporated, the creepage distance and clearance may be reduced below this value. (e.g. at a standard distance between soldering eye centers of 3.5mm).
 If this is not permissible, the user shall take suitable measures.

2. This photocoupler is suitable for 'safe electrical isolation' only within the safety limit data. Maintenance of the safety data shall be ensured by means of protective circuit.

VDE test sign: Marking on product

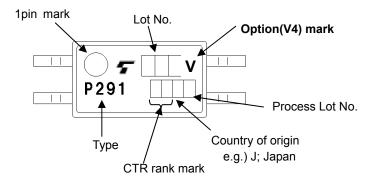
for EN 60747



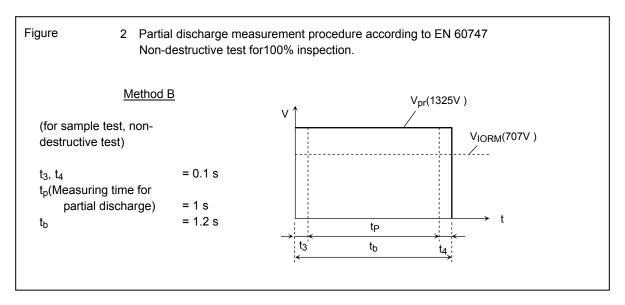
: Marking on packing for EN 60747

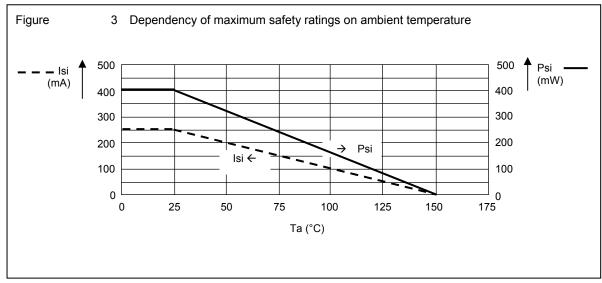


Marking Example: TLP291



Partial discharge measurement procedure according to EN 60747 Figure Destructive test for qualification and sampling tests. Method A V<sub>INITIAL</sub>(6kV) (for type and sampling tests, destructive tests) V<sub>pr</sub>(1060V) = 1 to 10 s t<sub>1.</sub> t<sub>2</sub> V<sub>IORM</sub>(707V) = 1 s $t_3$ ,  $t_4$ tp(Measuring time for partial discharge) = 10 stР t<sub>3</sub> = 12 s $t_b$ = 60 s $t_1$ t<sub>ini</sub>  $t_2$  $t_{b}$ t<sub>in<u>i</u></sub>





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