Silicon N Channel MOS Type (U-MOSⅢ)/Silicon Epitaxial Schottky Barrier Diode

SSM5H12TU

DC-DC Converter Applications

- 1.8-V drive
- Combined an N-ch MOSFET and a Schottky barrier diode in one package.
- Low R_{DS} (ON) and Low V_F

Absolute Maximum Ratings

MOSFET (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	30	V	
Gate-source voltage		V _{GSS}	± 12	V	
Drain current	DC	I _D	1.9	Α	
	Pulse	I _{DP}	3.8	A	
Power dissipation		P _D (Note 1)	0.5	W	
		t = 10s	0.8	VV	
Channel temperature		T _{ch}	150	°C	

Schottky Barrier Diode (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Repetitive peak reverse voltage	V_{RRM}	30	V
Average forward current	I _{F(AV)}	0.7	Α
Peak one cycle surge forward current	I _{FSM}	2 (50Hz)	Α
Junction temperature	Tj	125	°C

1: GATE 4: CATHODE 2: SOURCE 5: DRAIN 3: ANODE UFV JEDEC — JEITA — TOSHIBA 2-2R1A

Weight: 7 mg (typ.)

MOSFET and Diode (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Storage temperature range	T _{stg}	-55 to 125	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

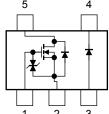
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Mounted on FR4 board

(25.4 mm \times 25.4 mm \times 1.6 mm, Cu pad: 645 mm²)

Marking 5 KEW

Equivalent Circuit (top view)



MOSFET

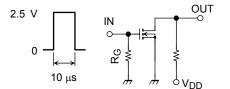
Electrical Characteristics (Ta = 25°C)

Chara	acteristic	Symbol	Test Conditions	Min	Тур.	Max	Unit
Drain-source breakdown voltage		V (BR) DSS	$I_D = 1 \text{ mA}, V_{GS} = 0 \text{ V}$	30	_	_	V
		V (BR) DSX	$I_D = 1$ mA, $V_{GS} = -12$ V	_	_	v	
Drain cut-off curre	ent	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	_	_	1	μΑ
Gate leakage curr	ent	I _{GSS}	$V_{GS} = \pm 12 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μΑ
Gate threshold vo	Itage	V _{th}	$V_{DS} = 3 \text{ V}, I_D = 1 \text{ mA}$	0.4	_	1.0	V
Forward transfer a	admittance	Yfs	$V_{DS} = 3 \text{ V}, I_D = 1.0 \text{ A}$ (Note	2) 2.0	3.9	_	S
		R _{DS (ON)}	$I_D = 1.0 \text{ A}, V_{GS} = 4.0 \text{ V}$ (Note	2) —	103	133	mΩ
Drain-source ON-resistance	$I_D = 0.8 \text{ A}, V_{GS} = 2.5 \text{ V}$ (Note		2) —	125	177		
			$I_D = 0.5 \text{ A}, V_{GS} = 1.8 \text{ V}$ (Note	2) —	165	296	
Input capacitance		C _{iss}			123	_	
Output capacitance		Coss	$V_{DS} = 15V, V_{GS} = 0 V, f = 1 MHz$		43	_	pF
Reverse transfer	capacitance	C _{rss}			18	_	
Total gate charge		Qg	Qg V 45V L 40A		1.9	_	
Gate-source charge Gate-drain charge		Q _{gs}	V _{DS} = 15V, I _D = 1.9 A V _{GS} = 4 V		1.1	_	nC
		Q _{gd}	VGS - 4 V		0.8	_	
Switching time	Turn-on time	t _{on}	V _{DD} = 15 V, I _D = 1.0 A, —	_	9.2	_	20
	Turn-off time	t _{off}	$V_{GS} = 0$ to 2.5 V, $R_G = 4.7 \Omega$		6.4	_	ns
Drain-source forward voltage		V_{DSF}	$I_D = -1.9 \text{ A}, V_{GS} = 0 \text{ V}$ (Note	2) —	-0.83	-1.2	V

Note 2: Pulse test

Switching Time Test Circuit

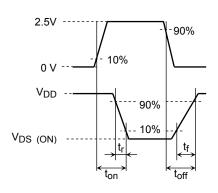




$$\begin{split} V_{DD} &= 15 \text{ V} \\ R_G &= 4.7 \text{ }\Omega \\ \text{Duty.} &\leq 1\% \\ V_{IN} &: t_r, \ t_f < 5 \text{ ns} \\ \text{Common Source} \\ Ta &= 25^{\circ}\text{C} \end{split}$$

(b) V_{IN}

(c) Vout



Precaution

 V_{th} can be expressed as voltage between gate and source when the low operating current value is $I_D = 1$ mA for this product. For normal switching operation, V_{GS} (on) requires a higher voltage than V_{th} and V_{GS} (off) requires a lower voltage than V_{th} .

(The relationship can be established as follows: $V_{GS\ (off)} < V_{th} < V_{GS\ (on)}$) Be sure to take this into consideration when using the device.

Schottky Barrier Diode

Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Peak forward voltage	V _{FM (1)}	I _F = 0.5 A	_	0.34	0.41	V
	V _{FM (2)}	I _F = 0.7 A	_	0.37	0.44	V
Repetitive peak reverse current	I _{RRM}	V _R = 15 V	_	60	200	μΑ
Total capacitance	C _T	V _R = 0 V, f = 1 MHz		139		pF

Precaution

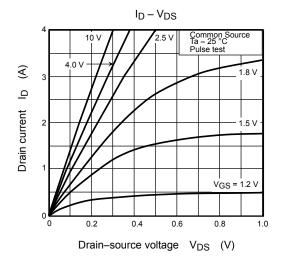
The Schottky barrier diode in this device has large reverse current leakage compared to typical switching diodes. Thus, excessive operating temperature or voltage may cause thermal runaway. To avoid this problem, be sure to take both forward and reverse loss into consideration.

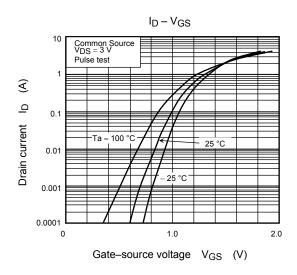
Handling Precaution

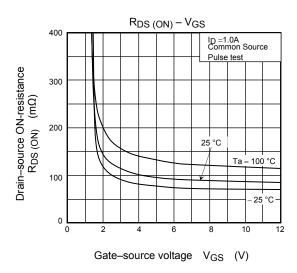
When handling individual devices (which are not yet mounted on a circuit board), ensure that the environment is protected against static electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

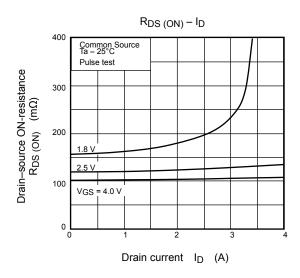
Thermal resistance $R_{th\ (j-a)}$ and power dissipation P_D vary depending on board material, board area, board thickness and pad area. When using this device, please take heat dissipation into consideration.

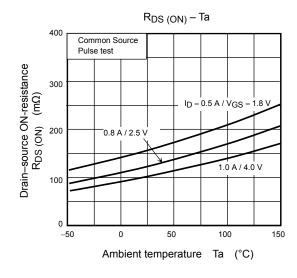
MOSFET

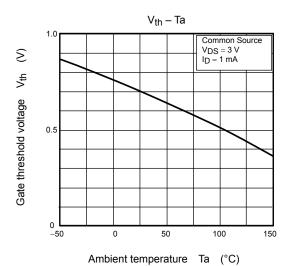




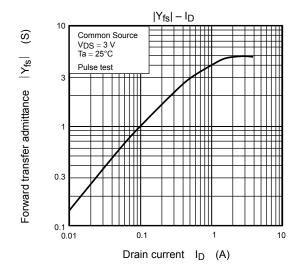


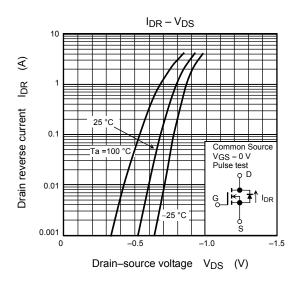


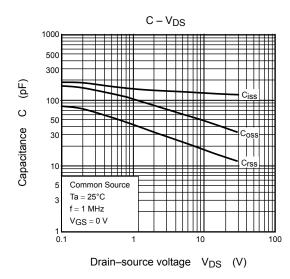


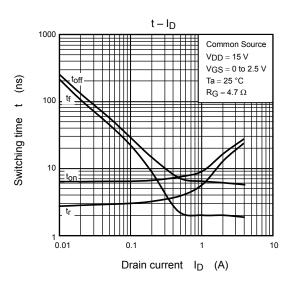


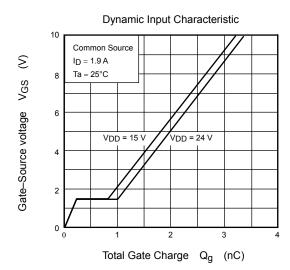
MOSFET





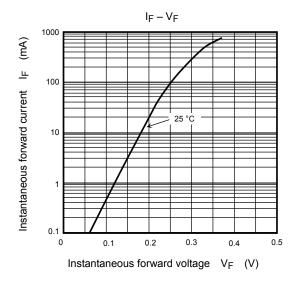


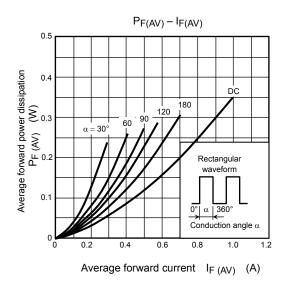


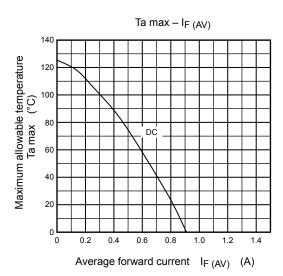


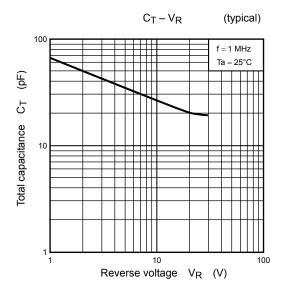
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Schottky Barrier Diode

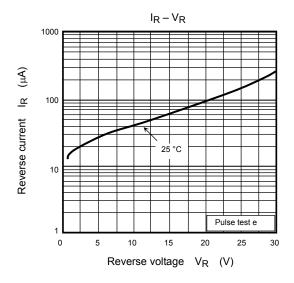


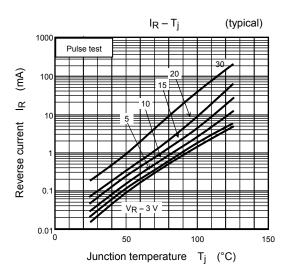


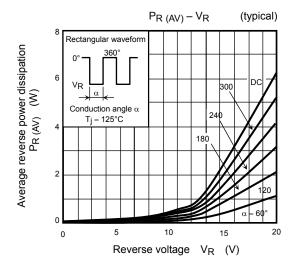




Schottky Barrier Diode







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