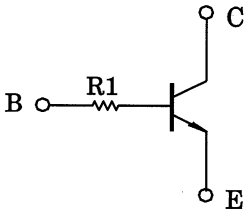
TOSHIBA Transistor Silicon NPN Epitaxial Type (PCT Process)

RN1112, RN1113

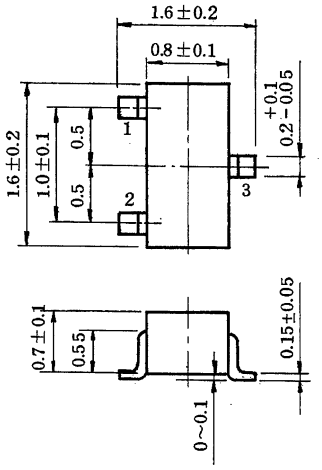
Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- With built-in bias resistors
- Simplified circuit design
- Reduced number of parts and simplified process
- Complementary to RN2112 and RN2113

Equivalent Circuit



Unit: mm



1. BASE
2. EMITTER
3. COLLECTOR

SSM

JEDEC	—
JEITA	—
TOSHIBA	2-2H1A

Weight: 2.4mg (typ.)

Absolute Maximum Ratings (Ta = 25°C)

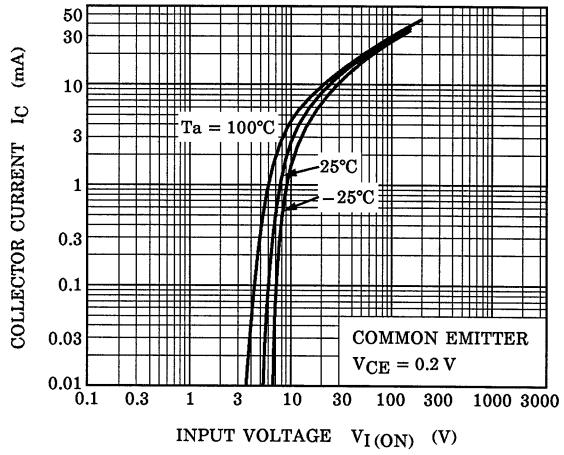
Characteristic	Symbol	Rating	Unit
Collector-base voltage	V_{CBO}	50	V
Collector-emitter voltage	V_{CEO}	50	V
Emitter-base voltage	V_{EBO}	5	V
Collector current	I_C	100	mA
Collector power dissipation	P_C	100	mW
Junction temperature	T_j	150	°C
Storage temperature range	T_{stg}	−55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

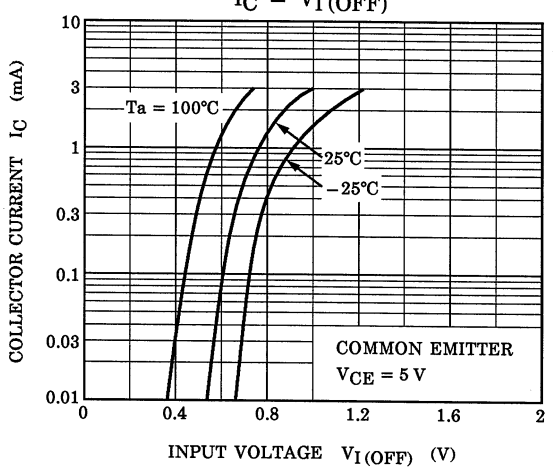
Electrical Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Collector cut-off current		I_{CBO}	—	V_{CB} = 50 V, I_E = 0	—	—	100	nA
Emitter cut-off current		I_{EBO}	—	V_{EB} = 5 V, I_C = 0	—	—	100	nA
DC current gain		h_{FE}	—	V_{CE} = 5 V, I_C = 1 mA	120	—	700	—
Collector-emitter saturation voltage		$V_{CE\ (sat)}$	—	I_C = 5 mA, I_B = 0.25 mA	—	0.1	0.3	V
Transition frequency		f_T	—	V_{CE} = 10 V, I_C = 5 mA	—	250	—	MHz
Collector output capacitance		C_{ob}	—	V_{CB} = 10 V, I_E = 0, f = 1 MHz	—	3	6	pF
Input resistor	RN1112	R1	—	—	15.4	22	28.6	kΩ
	RN1113				32.9	47	61.1	

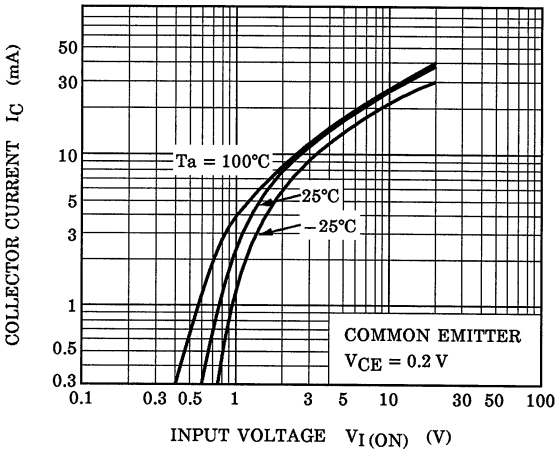
RN1112 $I_C - V_{I(ON)}$



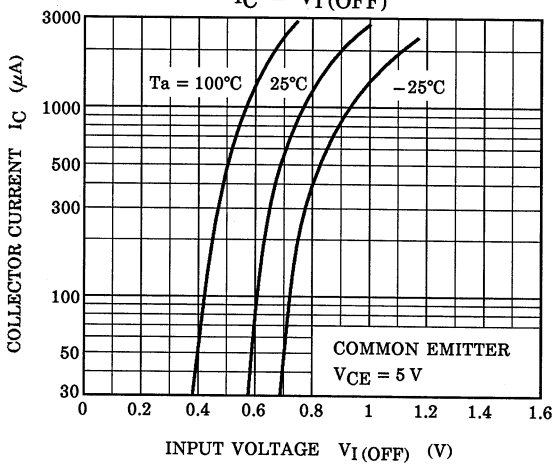
RN1112 $I_C - V_{I(OFF)}$



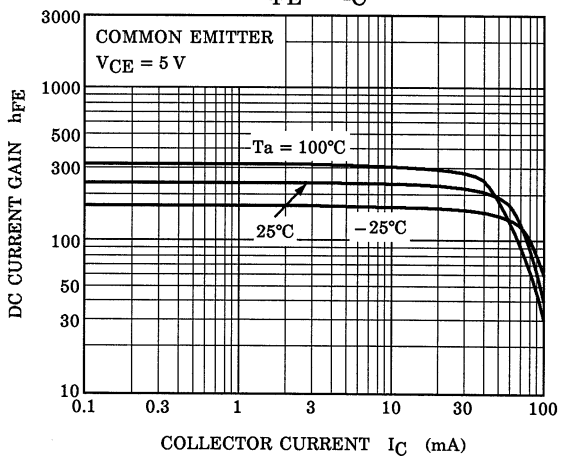
RN1113 $I_C - V_{I(ON)}$



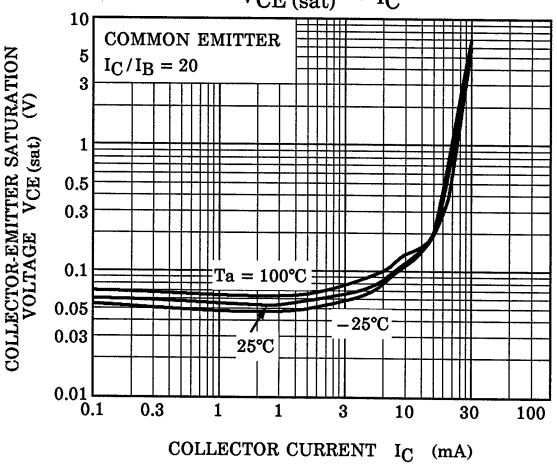
RN1113 $I_C - V_{I(OFF)}$



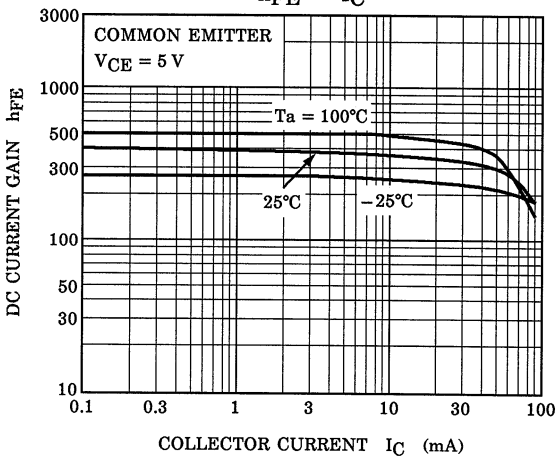
RN1112 h_{FE} – I_C



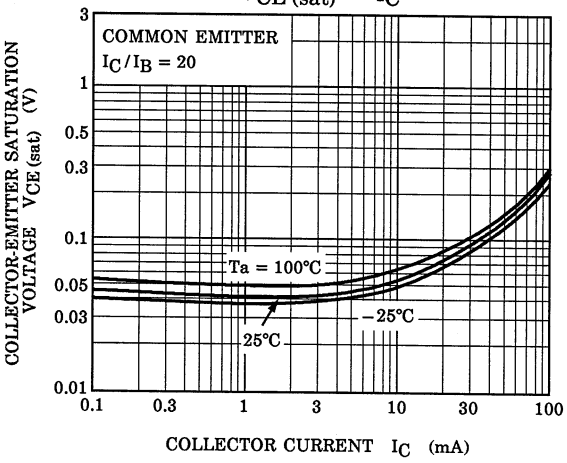
RN1112 $V_{CE\,(sat)}$ – I_C

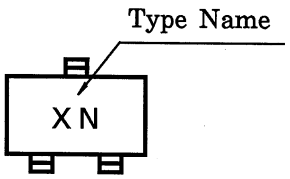
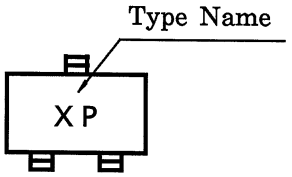


RN1113 h_{FE} – I_C



RN1113 $V_{CE\,(sat)}$ – I_C



Type Name	Marking
RN1112	
RN1113	

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