

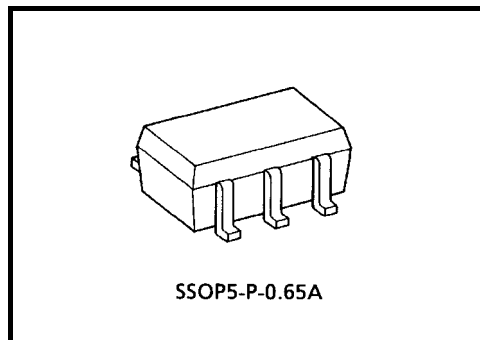
# TC7SBL66CFU,TC7SBL384CFU

## Low Voltage / Low Capacitance Single Bus Switch

The TC7SBL66C and TC7SBL384C are a Low Voltage / Low Capacitance CMOS single Bus Switch. The low On-resistance of the switch allows connections to be made with minimal propagation delay time.

The TC7SBL66C requires the output enable (OE) input to be set low to place the output into the high impedance state, whereas the TC7SBL384C requires the output enable ( $\overline{\text{OE}}$ ) input to be set high to place the output into the high impedance.

All inputs are equipped with protection circuits against static discharge.



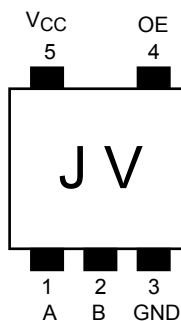
Weight: 0.006 g (typ.)

## Features

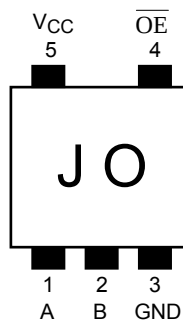
- Operating voltage :  $V_{CC} = 1.65$  to  $3.6$  V
- On-capacitance :  $C_{I/O} = 7$  pF Switch On (typ.)@ $V_{CC} = 3$  V
- On-resistance :  $R_{ON} = 5.5 \Omega$  (typ.)@ $V_{CC} = 3$  V,  $V_{I/O} = 0$  V
- ESD performance : Machine model  $\geq \pm 200$  V  
Human body model  $\geq \pm 2000$  V
- Power-down protection for inputs (OE and  $\overline{\text{OE}}$ , I/O)
- Package : USV

## Pin Assignment (top view)

TC7SBL66CFU



TC7SBL384CFU

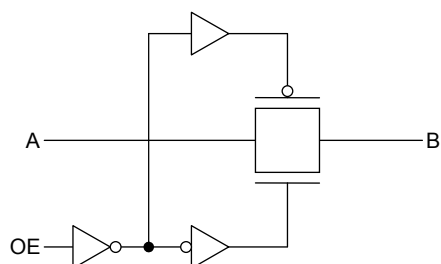


## Truth Table

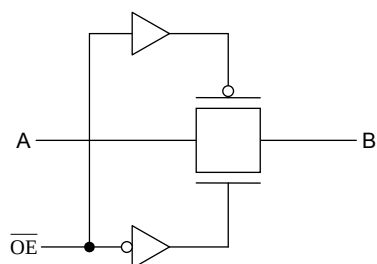
| Inputs (66) | Inputs (384)           | Function        |
|-------------|------------------------|-----------------|
| OE          | $\overline{\text{OE}}$ |                 |
| H           | L                      | A port = B port |
| L           | H                      | Disconnect      |

## System Diagram

TC7SBL66CFU



TC7SBL384CFU



## Absolute Maximum Ratings (Note)

| Characteristic                                    |                            | Symbol           | Rating               | Unit |
|---------------------------------------------------|----------------------------|------------------|----------------------|------|
| Power supply range                                |                            | $V_{CC}$         | −0.5 to 4.6          | V    |
| Control pin input voltage ( $\overline{OE}$ , OE) |                            | $V_{IN}$         | −0.5 to 4.6          | V    |
| Switch terminal I/O voltage                       | $V_{CC}=0$ V or Switch=Off | $V_S$            | −0.5 to 4.6          | V    |
|                                                   | Switch=On                  | $V_S$            | −0.5 to $V_{CC}+0.5$ |      |
| Clump diode current                               |                            | $I_{IK}$         | −50                  | mA   |
| Switch I/O current                                |                            | $I_S$            | 50                   | mA   |
| Power dissipation                                 |                            | $P_D$            | 200                  | mW   |
| DC $V_{CC}$ /GND current                          |                            | $I_{CC}/I_{GND}$ | ±100                 | mA   |
| Storage temperature                               |                            | $T_{stg}$        | −65 to 150           | °C   |

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

## Operating Ranges (Note)

| Characteristic                                    |                            | Symbol    | Rating        | Unit |
|---------------------------------------------------|----------------------------|-----------|---------------|------|
| Power supply voltage                              |                            | $V_{CC}$  | 1.65 to 3.6   | V    |
| Control pin input voltage ( $\overline{OE}$ , OE) |                            | $V_{IN}$  | 0 to 3.6      | V    |
| Switch terminal I/O voltage                       | $V_{CC}=0$ V or Switch=Off | $V_S$     | 0 to 3.6      | V    |
|                                                   | Switch=On                  | $V_S$     | 0 to $V_{CC}$ |      |
| Operating temperature                             |                            | $T_{opr}$ | −40 to 85     | °C   |
| Input rise and fall time                          |                            | $dt/dv$   | 0 to 10       | ns/V |

Note: The operating ranges must be maintained to ensure the normal operation of the device.  
Unused inputs must be tied to either  $V_{CC}$  or GND.

## Electrical Characteristics

## DC Characteristics (Ta = -40 to 85°C)

| Parameter                                              |                 | Symbol           | Test Condition                                                                                 | V <sub>CC</sub> (V) | Min                 | Typ.<br>(Note1) | Max                 | Unit |
|--------------------------------------------------------|-----------------|------------------|------------------------------------------------------------------------------------------------|---------------------|---------------------|-----------------|---------------------|------|
|                                                        |                 |                  |                                                                                                |                     |                     |                 |                     |      |
| Input voltage<br>(OE, $\overline{\text{OE}}$ )         | "H" level       | V <sub>IH</sub>  | —                                                                                              | 1.65 to 3.6         | $0.7 \times V_{CC}$ | —               | —                   | V    |
|                                                        | "L" level       | V <sub>IL</sub>  | —                                                                                              | 1.65 to 3.6         | —                   | —               | $0.3 \times V_{CC}$ |      |
| Input leakage current<br>(OE, $\overline{\text{OE}}$ ) |                 | I <sub>IN</sub>  | V <sub>IN</sub> = 0 to 3.6V                                                                    | 1.65 to 3.6         | —                   | —               | ±1.0                | μA   |
| Power-off leakage current                              |                 | I <sub>OFF</sub> | OE, $\overline{\text{OE}}$ , A, B = 0 to 3.6 V                                                 | 0                   | —                   | —               | 10                  | μA   |
| Off-state leakage current<br>(switch off)              |                 | I <sub>SZ</sub>  | A, B = 0 to V <sub>CC</sub> ,<br>OE = GND (66), $\overline{\text{OE}}$ = V <sub>CC</sub> (384) | 1.65 to 3.6         | —                   | —               | ±1.0                | μA   |
| On resistance<br>(Note2)                               | R <sub>ON</sub> |                  | V <sub>IS</sub> = 0 V, I <sub>S</sub> = 30 mA (Note 1)                                         | 3.0                 | —                   | 5.5             | 10                  | Ω    |
|                                                        |                 |                  | V <sub>IS</sub> = 3.0 V, I <sub>S</sub> = 30 mA (Note 1)                                       | 3.0                 | —                   | 10              | 16                  |      |
|                                                        |                 |                  | V <sub>IS</sub> = 2.4V, I <sub>S</sub> = 15 mA (Note 1)                                        | 3.0                 | —                   | 12              | 18                  |      |
|                                                        |                 |                  | V <sub>IS</sub> = 0 V, I <sub>S</sub> = 24 mA (Note 1)                                         | 2.3                 | —                   | 6               | 10                  |      |
|                                                        |                 |                  | V <sub>IS</sub> = 2.3 V, I <sub>S</sub> = 24 mA (Note 1)                                       | 2.3                 | —                   | 13              | 20                  |      |
|                                                        |                 |                  | V <sub>IS</sub> = 2.0V, I <sub>S</sub> = 15 mA (Note 1)                                        | 2.3                 | —                   | 15              | 21                  |      |
|                                                        |                 |                  | V <sub>IS</sub> = 0 V, I <sub>S</sub> = 4 mA (Note 1)                                          | 1.65                | —                   | 7               | 13                  |      |
|                                                        |                 |                  | V <sub>IS</sub> = 1.65 V, I <sub>S</sub> = 4 mA (Note 1)                                       | 1.65                | —                   | 18              | 27                  |      |
| Quiescent supply current                               |                 | I <sub>CC</sub>  | V <sub>IN</sub> = V <sub>CC</sub> or GND, I <sub>OUT</sub> = 0                                 | 3.6                 | —                   | —               | 10                  | μA   |

Note 1: All typical values are at Ta=25°C.

Note 2: Measured by the voltage drop between A and B pins at the indicated current through the switch.

On resistance is determined by the lower of the voltages on the two (A or B) pins.

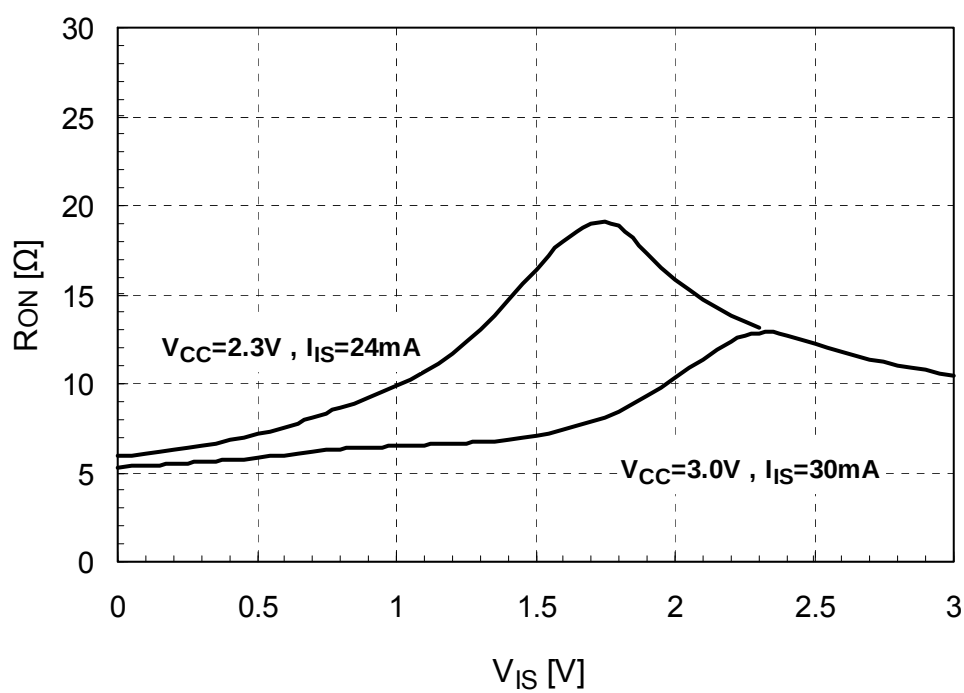
## AC Characteristics (Ta = -40 to 85°C)

| Characteristics     | Symbol                               | Test Condition     | V <sub>CC</sub> (V) | Min | Max | Unit |
|---------------------|--------------------------------------|--------------------|---------------------|-----|-----|------|
|                     |                                      |                    |                     |     |     |      |
| Output enable time  | t <sub>pZL</sub><br>t <sub>pZH</sub> | Figure 1, Figure 2 | 3.3±0.3             | —   | 6   | ns   |
|                     |                                      |                    | 2.5±0.2             | —   | 7   |      |
|                     |                                      |                    | 1.8±0.15            | —   | 11  |      |
| Output disable time | t <sub>pLZ</sub><br>t <sub>pHZ</sub> | Figure 1, Figure 2 | 3.3±0.3             | —   | 6   | ns   |
|                     |                                      |                    | 2.5±0.2             | —   | 7   |      |
|                     |                                      |                    | 1.8±0.15            | —   | 11  |      |

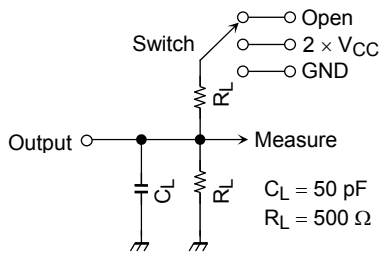
## Capacitive Characteristics (Ta = 25°C)

| Characteristics                             | Symbol           | Test Condition                                                                              | V <sub>CC</sub> (V) | Typ. | Unit |
|---------------------------------------------|------------------|---------------------------------------------------------------------------------------------|---------------------|------|------|
|                                             |                  |                                                                                             |                     |      |      |
| Control pin input capacitance               | C <sub>IN</sub>  | V <sub>IN</sub> = 0 V (Note)                                                                | 3.0                 | 4    | pF   |
| Switch terminal capacitance<br>(Switch Off) | C <sub>I/O</sub> | OE = GND (66), $\overline{\text{OE}}$ = V <sub>CC</sub> (384), V <sub>IS</sub> = 0 V (Note) | 3.0                 | 3.5  | pF   |
| Switch terminal capacitance<br>(Switch On)  | C <sub>I/O</sub> | OE = V <sub>CC</sub> (66), $\overline{\text{OE}}$ = GND (384), V <sub>IS</sub> = 0 V (Note) | 3.0                 | 7    | pF   |

Note: This parameter is guaranteed by design.

$R_{ON}$  -  $V_{IS}$  Characteristic (typ.)  $T_a=25^\circ\text{C}$ 

AC Test Circuit



| Parameter          | Switch            |
|--------------------|-------------------|
| $t_{pLZ}, t_{pZL}$ | $2 \times V_{CC}$ |
| $t_{pHZ}, t_{pZH}$ | GND               |

Figure 1

AC Waveform

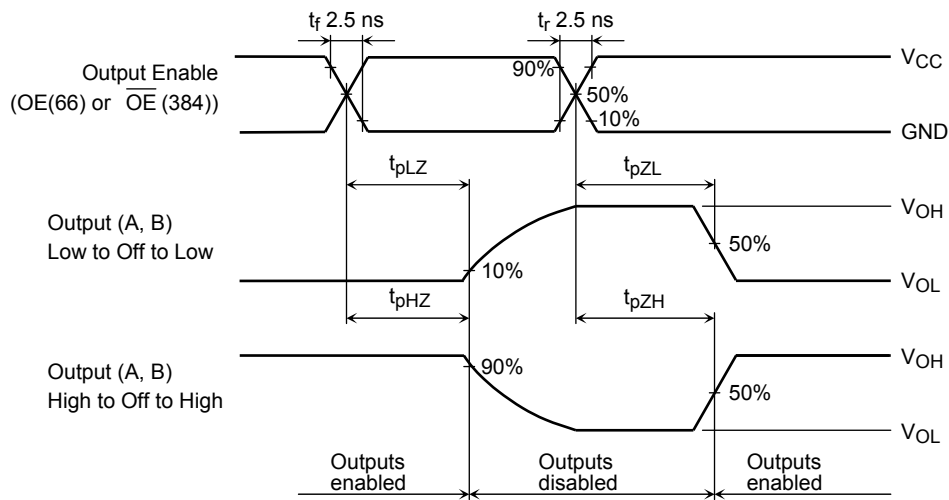


Figure 2  $t_{pLZ}, t_{pHZ}, t_{pZL}, t_{pZH}$

## Rise and Fall Times ( $t_r$ / $t_f$ ) of the TC7SBL66C, 384C I/O Signals

The  $t_r(\text{out})$  and  $t_f(\text{out})$  values of the output signals are affected by the CR time constant of the input, which consists of the switch terminal capacitance ( $C_{I/O}$ ) and the on-resistance ( $R_{ON}$ ) of the input.

In practice, the  $t_r(\text{out})$  and  $t_f(\text{out})$  values are also affected by the circuit's capacitance and resistance components other than those of the TC7SBL66CFU, 384CFU.

The  $t_r(\text{out})$  /  $t_f(\text{out})$  values can be approximated as follows. (Figure 3 shows the test circuit.)

$$t_r(\text{out}) / t_f(\text{out}) (\text{approx}) = -(C_{I/O} + C_L) \cdot (R_{DRIVE} + R_{ON}) \cdot \ln(((V_{OH} - V_{OL}) - V_M) / (V_{OH} - V_{OL}))$$

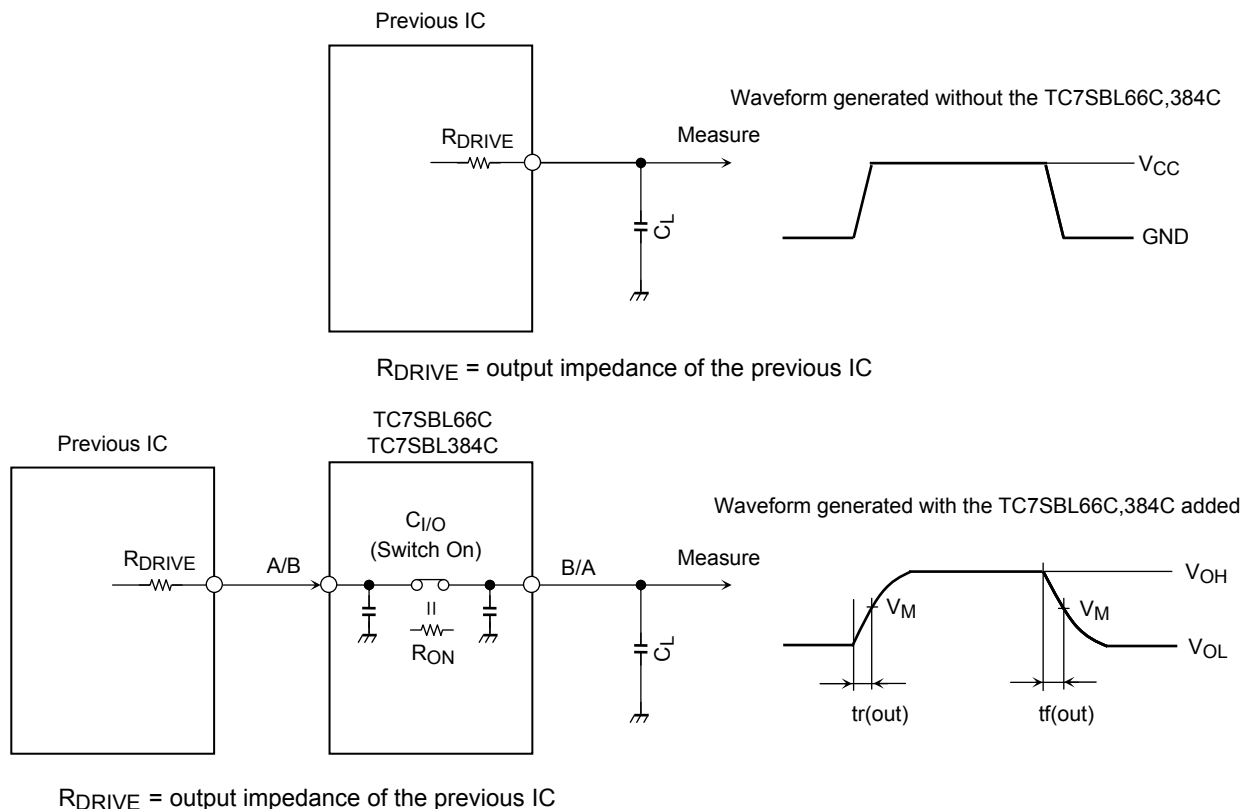
where,  $R_{DRIVE}$  is the output impedance of the previous-stage circuit.

Calculation example:

$$t_r(\text{out}) (\text{approx}) = -(7 + 15) \times 10^{-12} \cdot (120 + 5.5) \cdot \ln(((3.0 - 0) - 1.5) / (3.0 - 0)) \\ \approx 1.9 \text{ ns}$$

Calculation conditions:

$V_{CC} = 3.0 \text{ V}$ ,  $C_L = 15 \text{ pF}$ ,  $R_{DRIVE} = 120 \Omega$  (output impedance of the previous IC),  $V_M = 1.5 \text{ V}$  ( $V_{CC} / 2$ )  
Output voltage of the previous IC = digital (i.e., high-level voltage =  $V_{CC}$ ; low-level voltage = GND)



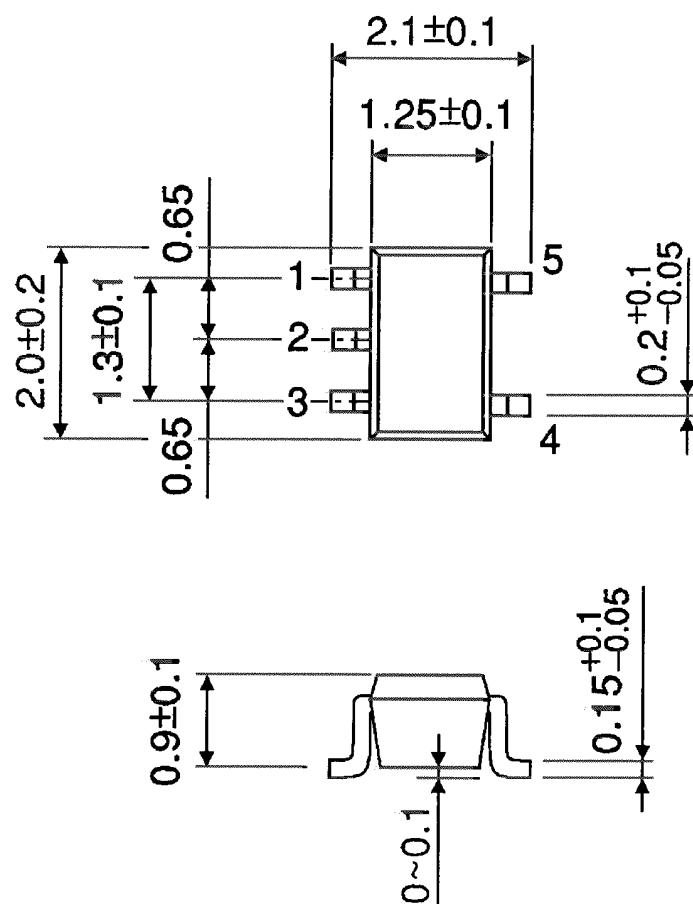
| Parameter | $V_{CC}$                |                         |                          |
|-----------|-------------------------|-------------------------|--------------------------|
|           | $3.3 \pm 0.3 \text{ V}$ | $2.5 \pm 0.2 \text{ V}$ | $1.8 \pm 0.15 \text{ V}$ |
| $V_M$     | $V_{CC} / 2$            | $V_{CC} / 2$            | $V_{CC} / 2$             |

**Figure 3 Test Circuit**

**Package Dimensions**

SSOP5-P-0.65A

Unit : mm



Weight: 0.006 g (typ.)



**RESTRICTIONS ON PRODUCT USE**

- Toshiba Corporation, and its subsidiaries and affiliates (collectively "TOSHIBA"), reserve the right to make changes to the information in this document, and related hardware, software and systems (collectively "Product") without notice.
- This document and any information herein may not be reproduced without prior written permission from TOSHIBA. Even with TOSHIBA's written permission, reproduction is permissible only if reproduction is without alteration/omission.
- Though TOSHIBA works continually to improve Product's quality and reliability, Product can malfunction or fail. Customers are responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which minimize risk and avoid situations in which a malfunction or failure of Product could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Before creating and producing designs and using, customers must also refer to and comply with (a) the latest versions of all relevant TOSHIBA information, including without limitation, this document, the specifications, the data sheets and application notes for Product and the precautions and conditions set forth in the "TOSHIBA Semiconductor Reliability Handbook" and (b) the instructions for the application that Product will be used with or for. Customers are solely responsible for all aspects of their own product design or applications, including but not limited to (a) determining the appropriateness of the use of this Product in such design or applications; (b) evaluating and determining the applicability of any information contained in this document, or in charts, diagrams, programs, algorithms, sample application circuits, or any other referenced documents; and (c) validating all operating parameters for such designs and applications. **TOSHIBA ASSUMES NO LIABILITY FOR CUSTOMERS' PRODUCT DESIGN OR APPLICATIONS.**
- Product is intended for use in general electronics applications (e.g., computers, personal equipment, office equipment, measuring equipment, industrial robots and home electronics appliances) or for specific applications as expressly stated in this document. Product is neither intended nor warranted for use in equipment or systems that require extraordinarily high levels of quality and/or reliability and/or a malfunction or failure of which may cause loss of human life, bodily injury, serious property damage or serious public impact ("Unintended Use"). Unintended Use includes, without limitation, equipment used in nuclear facilities, equipment used in the aerospace industry, medical equipment, equipment used for automobiles, trains, ships and other transportation, traffic signaling equipment, equipment used to control combustions or explosions, safety devices, elevators and escalators, devices related to electric power, and equipment used in finance-related fields. Do not use Product for Unintended Use unless specifically permitted in this document.
- Do not disassemble, analyze, reverse-engineer, alter, modify, translate or copy Product, whether in whole or in part.
- Product shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable laws or regulations.
- The information contained herein is presented only as guidance for Product use. No responsibility is assumed by TOSHIBA for any infringement of patents or any other intellectual property rights of third parties that may result from the use of Product. No license to any intellectual property right is granted by this document, whether express or implied, by estoppel or otherwise.
- **ABSENT A WRITTEN SIGNED AGREEMENT, EXCEPT AS PROVIDED IN THE RELEVANT TERMS AND CONDITIONS OF SALE FOR PRODUCT, AND TO THE MAXIMUM EXTENT ALLOWABLE BY LAW, TOSHIBA (1) ASSUMES NO LIABILITY WHATSOEVER, INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR LOSS, INCLUDING WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND LOSS OF DATA, AND (2) DISCLAIMS ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO SALE, USE OF PRODUCT, OR INFORMATION, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, ACCURACY OF INFORMATION, OR NONINFRINGEMENT.**
- Do not use or otherwise make available Product or related software or technology for any military purposes, including without limitation, for the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile technology products (mass destruction weapons). Product and related software and technology may be controlled under the Japanese Foreign Exchange and Foreign Trade Law and the U.S. Export Administration Regulations. Export and re-export of Product or related software or technology are strictly prohibited except in compliance with all applicable export laws and regulations.
- Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. Please use Product in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. TOSHIBA assumes no liability for damages or losses occurring as a result of noncompliance with applicable laws and regulations.