

TC7MBL3245BFT, TC7MBL3245BFK

Octal Low Voltage Bus Switch

The TC7MBL3245B provides eight bits of low-voltage, high-speed bus switching in a standard '245 device pinout. The low ON-resistance of the switch allows connections to be made with minimal propagation delay and while maintaining CMOS low power dissipation.

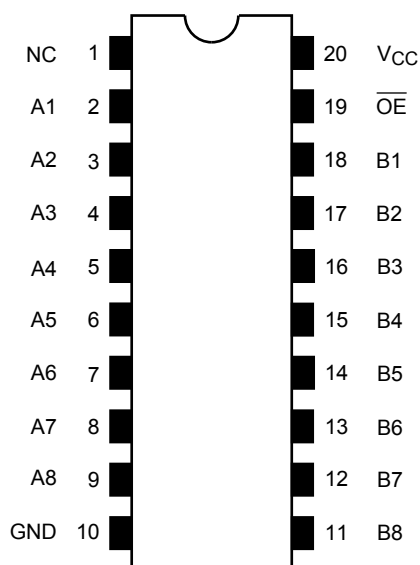
The device comprises a single 8-bit switch. When output enable ($\overline{OE}$) is low, the switch is on and port A is connected to port B. When $\overline{OE}$ is high, the switch is open and a high-impedance state exists between the two ports.

All inputs are equipped with protection circuits to guard against static discharge.

Features

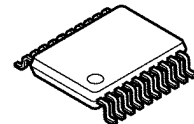
- Operating voltage: $V_{CC} = 1.65 \sim 3.6 \text{ V}$
- Low capacitance: $C_{I/O} = 19 \text{ pF}$ Switch On (typ.) @3V
- Low ON-resistance: $R_{ON} = 4 \Omega$ (typ.) @3V
- ESD performance: Machine model $\geq \pm 200 \text{ V}$
Human body model $\geq \pm 2000 \text{ V}$
- Power-down protection for inputs ($\overline{OE}$ input only)
- Package: TSSOP20, VSSOP (US20)
- Pin compatible with the 74xx245 type

Pin Assignment (top view)



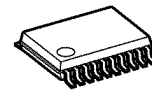
NC-No Internal Connection

TC7MBL3245BFT



TSSOP20-P-0044-0.65A

TC7MBL3245BFK



VSSOP20-P-0030-0.50

Weight:

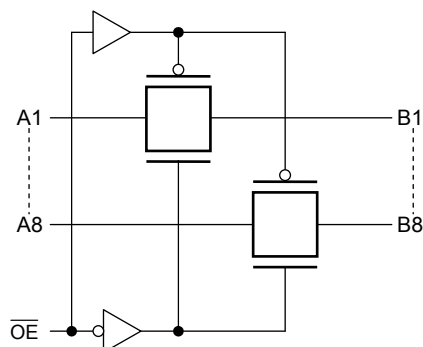
TSSOP20-P-0044-0.65A : 0.08 g (typ.)

VSSOP20-P-0030-0.50 : 0.03 g (typ.)

Truth Table

Inputs	Function
OE	
L	A port = B port
H	Disconnect

System Diagram



Absolute Maximum Ratings (Note)

Characteristic	Symbol	Rating	Unit
Power supply range	V_{CC}	-0.5~4.6	V
Control pin input voltage	V_{IN}	-0.5~4.6	V
Switch terminal I/O voltage	V_S	-0.5~ $V_{CC}+0.5$	V
Clump diode current	Control input pin	-50	mA
	Switch terminal	±50	
Switch I/O current	I_S	128	mA
Power dissipation	P_D	180	mW
DC V_{CC}/GND current	I_{CC}/I_{GND}	±100	mA
Storage temperature	T_{stg}	-65~150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (Note)

Characteristic	Symbol	Rating	Unit
Power supply voltage	V_{CC}	1.65~3.6	V
Control pin input voltage	V_{IN}	0~3.6	V
Switch I/O voltage	V_S	0~ V_{CC}	V
Operating temperature	T_{opr}	-40~85	°C
Input rise and fall time	dt/dv	0~10	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device.

Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C)

Characteristic	Symbol	Test Condition	V _{CC} (V)	Min	Typ.	Max	Unit
High-level control input voltage	V _{IH}	—	1.65 to 3.6	0.7 × V _{CC}	—	—	V
Low-level control input voltage	V _{IL}	—	1.65 to 3.6	—	—	0.3 × V _{CC}	
Control input current	I _{IN}	V _{IN} = 0 to 3.6 V	1.65 to 3.6	—	—	±1.0	μA
Power off leakage current	I _{OFF}	$\overline{\text{OE}}$ = 0 to 3.6 V	0	—	—	1.0	μA
Off-stage leakage current (switch off)	I _{SZ}	A, B = 0 to V _{CC} , $\overline{\text{OE}}$ = V _{CC}	1.65 to 3.6	—	—	±1.0	μA
Switch ON-resistance (Note 2)	R _{ON}	V _{IS} = 0 V, I _{IS} = 30 mA (Note 1)	3.0	—	4	7	Ω
		V _{IS} = 3.0 V, I _{IS} = 30 mA (Note 1)	3.0	—	6	9	
		V _{IS} = 2.4 V, I _{IS} = 15 mA (Note 1)	3.0	—	7	12	
		V _{IS} = 0 V, I _{IS} = 24 mA (Note 1)	2.3	—	4	8	
		V _{IS} = 2.3 V, I _{IS} = 24 mA (Note 1)	2.3	—	7	11	
		V _{IS} = 2.0 V, I _{IS} = 24 mA (Note 1)	2.3	—	8	13	
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND, I _{OUT} = 0	3.6	—	—	10	μA

Note 1: All typical values are at Ta = 25°C.

Note 2: Measured by voltage drop between A and B pins at indicated current through the switch. ON-resistance is determined by the lower of the voltages on the two pins (A or B).

AC Characteristics (Ta = -40 to 85°C)

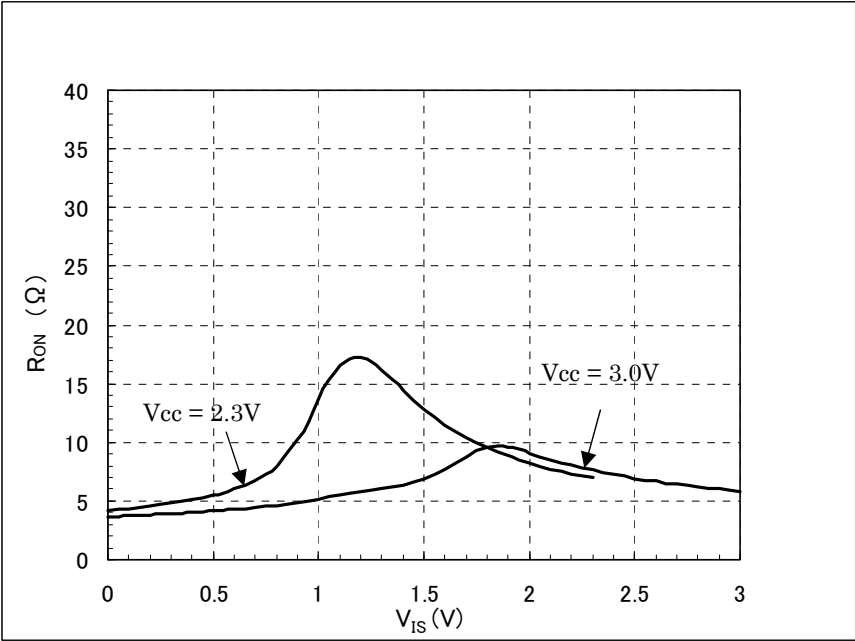
Characteristic	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Output enable time	t _{pZL} t _{pZH}	Figure 1, Figure 2	3.3 ± 0.3	—	6	ns
			2.5 ± 0.2	—	7	
			1.8 ± 0.15	—	11	
Output disable time	t _{pLZ} t _{pHZ}	Figure 1, Figure 2	3.3 ± 0.3	—	6	ns
			2.5 ± 0.2	—	7	
			1.8 ± 0.15	—	11	

Capacitive Characteristics (Ta = 25°C)

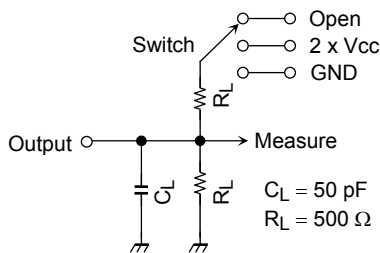
Characteristic (Note)	Symbol	Test Condition	V _{CC} (V)	Typ.	Unit
Control input capacitance	C _{IN}		3.0	3	pF
Switch terminal capacitance	C _{I/O}	$\overline{\text{OE}}$ = V _{CC} Switch Off	3.0	9	pF
		$\overline{\text{OE}}$ = GND Switch On	3.0	19	pF

Note: This parameter is guaranteed by design.

$R_{ON} - V_{IN}$ Characteristic Curves (Typ.) , $T_a = 25^{\circ}\text{C}$



AC Test Circuit



Parameter	Switch
t_{pLH}, t_{pHL}	Open
t_{pLZ}, t_{pZL}	$2 \times V_{CC}$
t_{pHZ}, t_{pZH}	GND

Figure 1

AC Waveforms

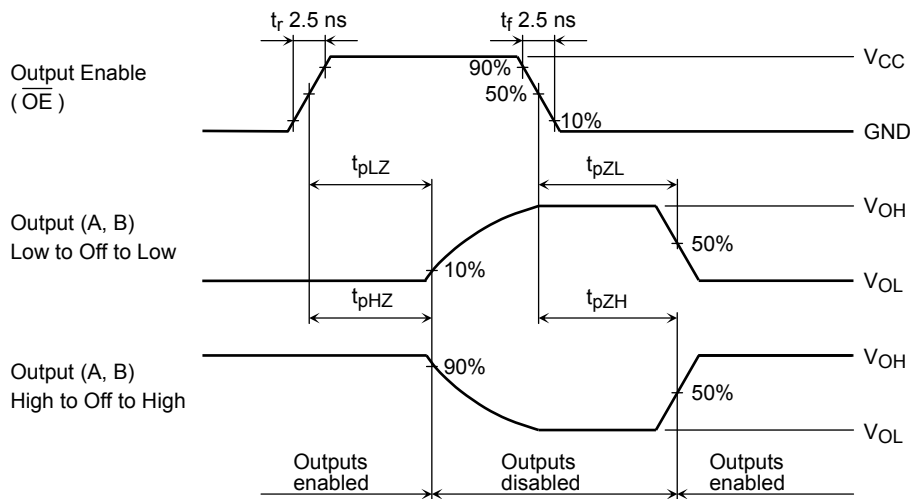


Figure 2 $t_{pLZ}, t_{pHZ}, t_{pZL}, t_{pZH}$

Rise and Fall Times (tr / tf) of the TC7MBL3245B I/O Signals

The tr(out) and tf(out) values of the output signals are affected by the CR time constant of the input, which consists of the switch terminal capacitance (C_{I/O}) and the on-resistance (R_{ON}) of the input.

In practice, the tr(out) and tf(out) values are also affected by the circuit's capacitance and resistance components other than those of the TC7MBL3245B.

The tr / tf (out) values can be approximated as follows. (Figure 4 shows the test circuit.)

$$tr / tf \text{ out (approx)} = - (C_{I/O} + C_L) \cdot (R_{DRIVE} + R_{ON}) \cdot \ln \left(\frac{(V_{OH} - V_{OL}) - V_M}{(V_{OH} - V_{OL})} \right)$$

where, R_{DRIVE} is the output impedance of the previous-stage circuit.

Calculation example:

$$tr \text{ out (approx)} = - (19 + 15) \times 10^{-12} \cdot (120 + 4) \cdot \ln \left(\frac{(3.0 - 0) - 1.5}{(3.0 - 0)} \right) \approx 3.0 \text{ ns}$$

Calculation conditions:

V_{CC} = 3.0V, C_L = 15pF, R_{DRIVE} = 120Ω (output impedance of the previous IC), V_M = 1.5V (V_{CC} / 2)

Output of the previous IC = digital (i.e., high-level voltage = V_{CC}; low-level voltage = GND)

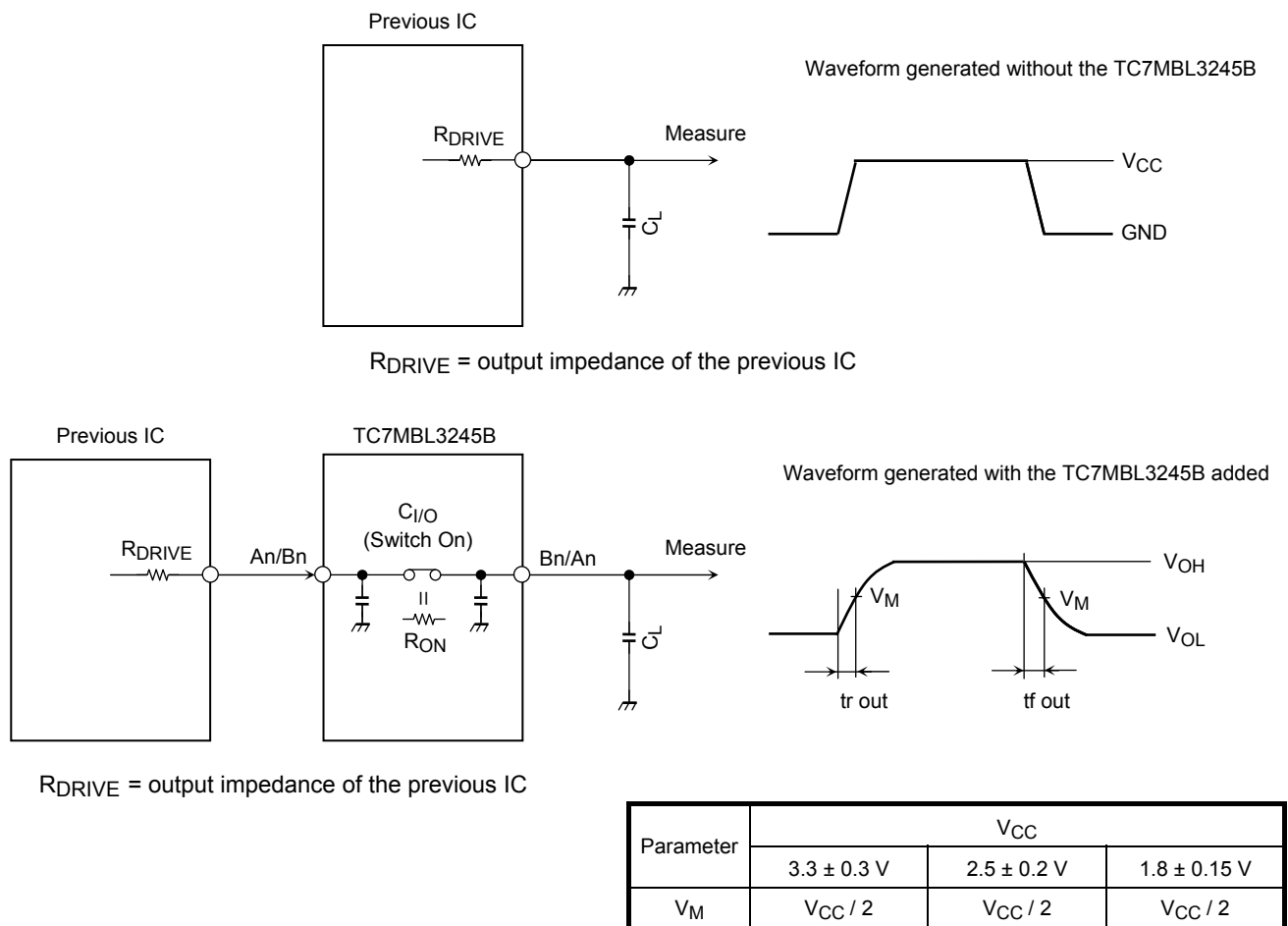
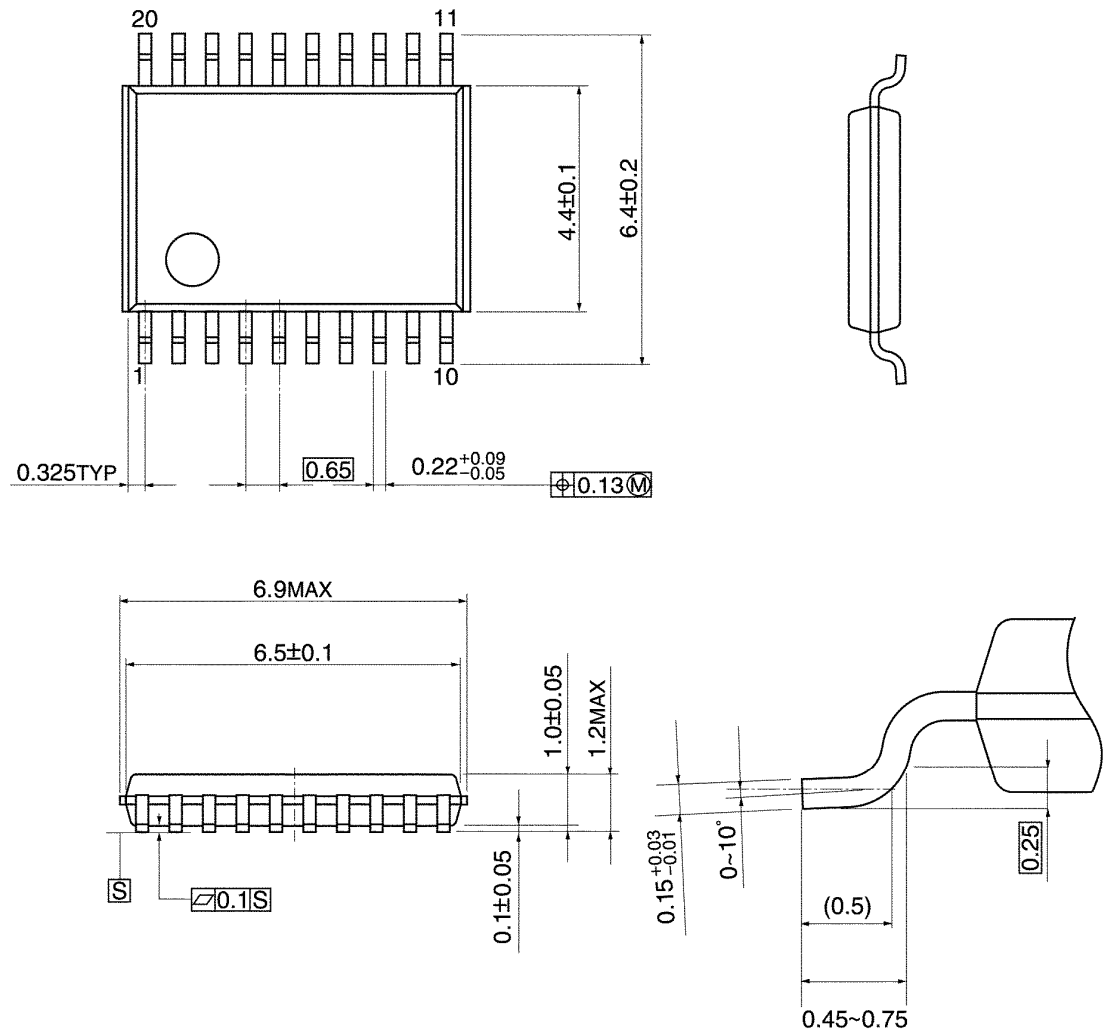


Figure 3 Test Circuit

Package Dimensions

TSSOP20-P-0044-0.65A

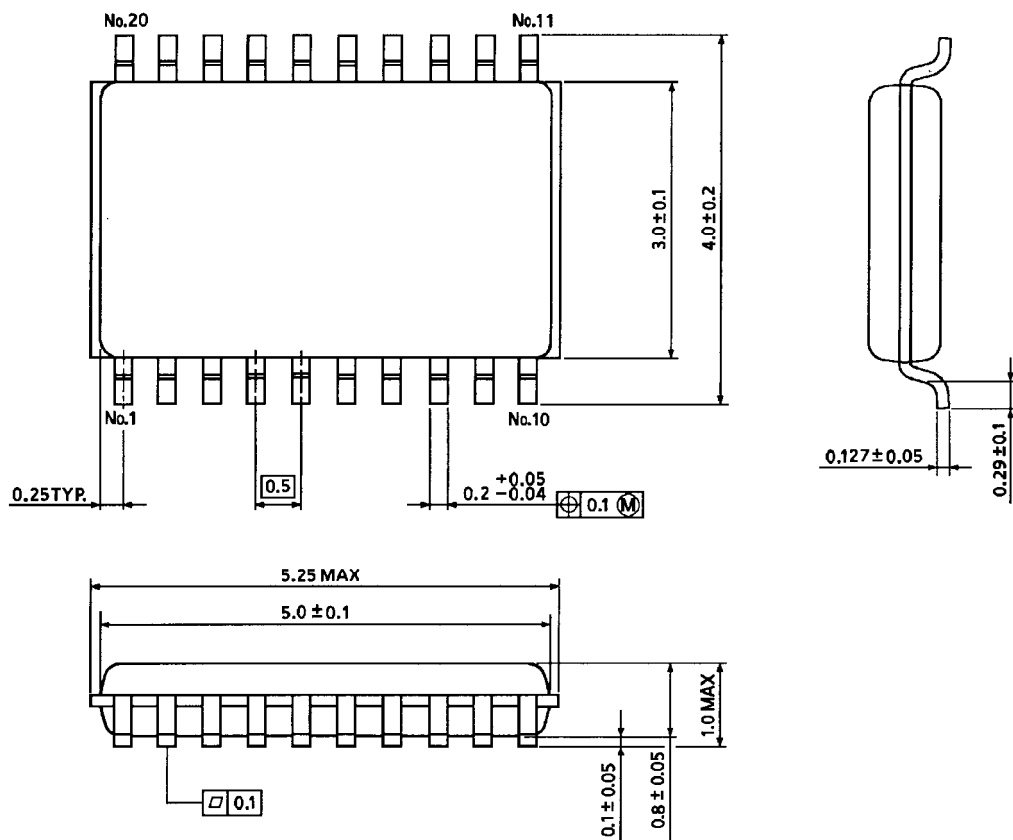
Unit: mm



Weight: 0.08 g (typ.)

VSSOP20-P-0030-0.50

Unit : mm



Weight: 0.03 g (typ.)

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